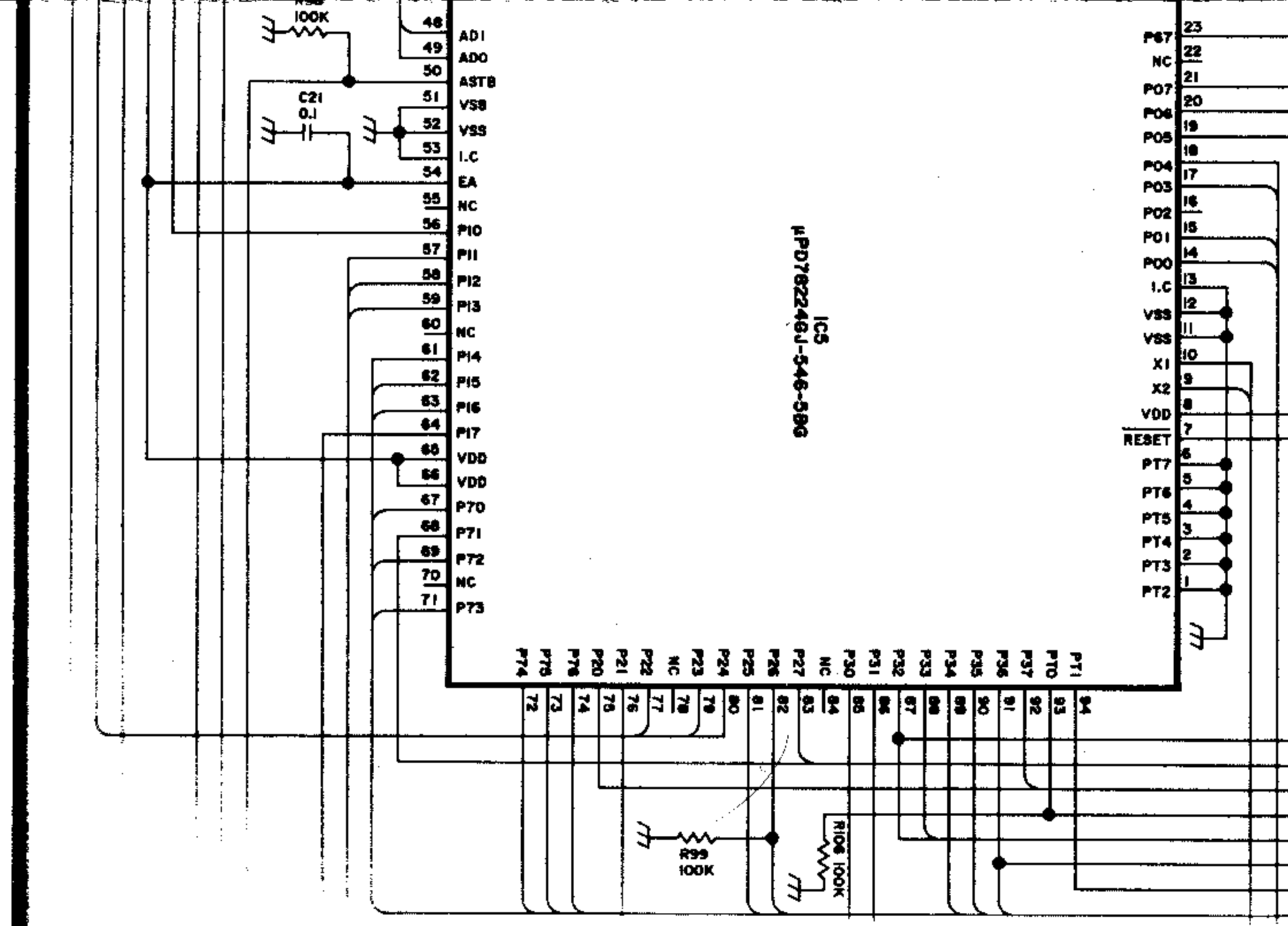
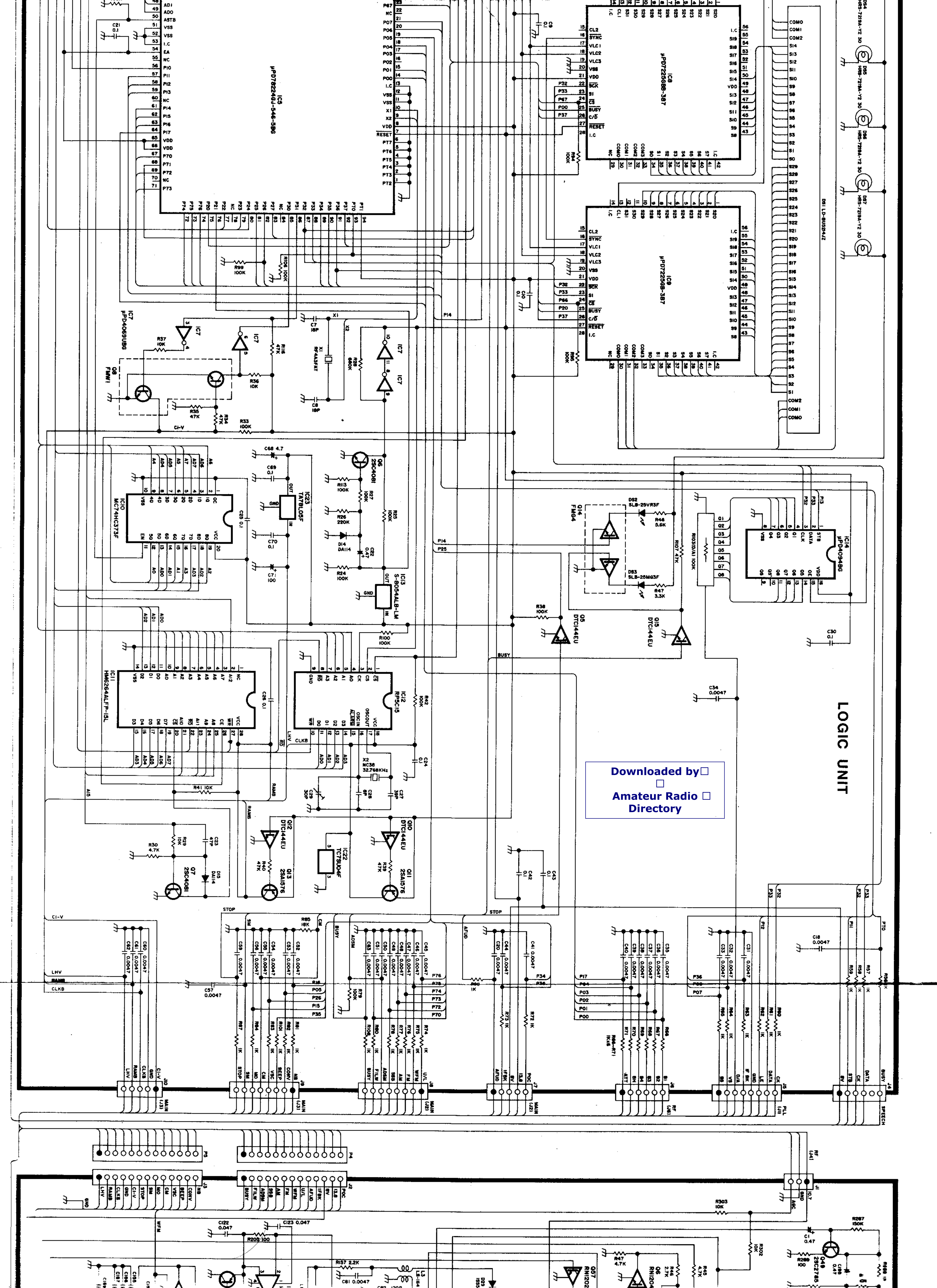
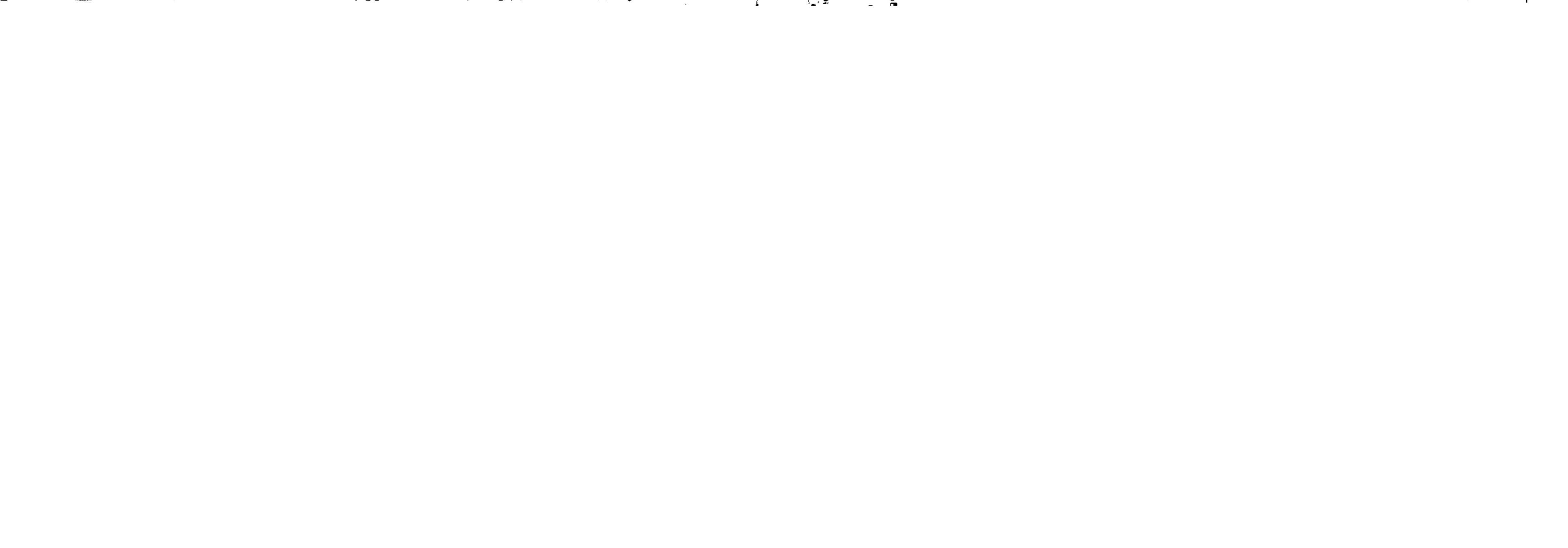
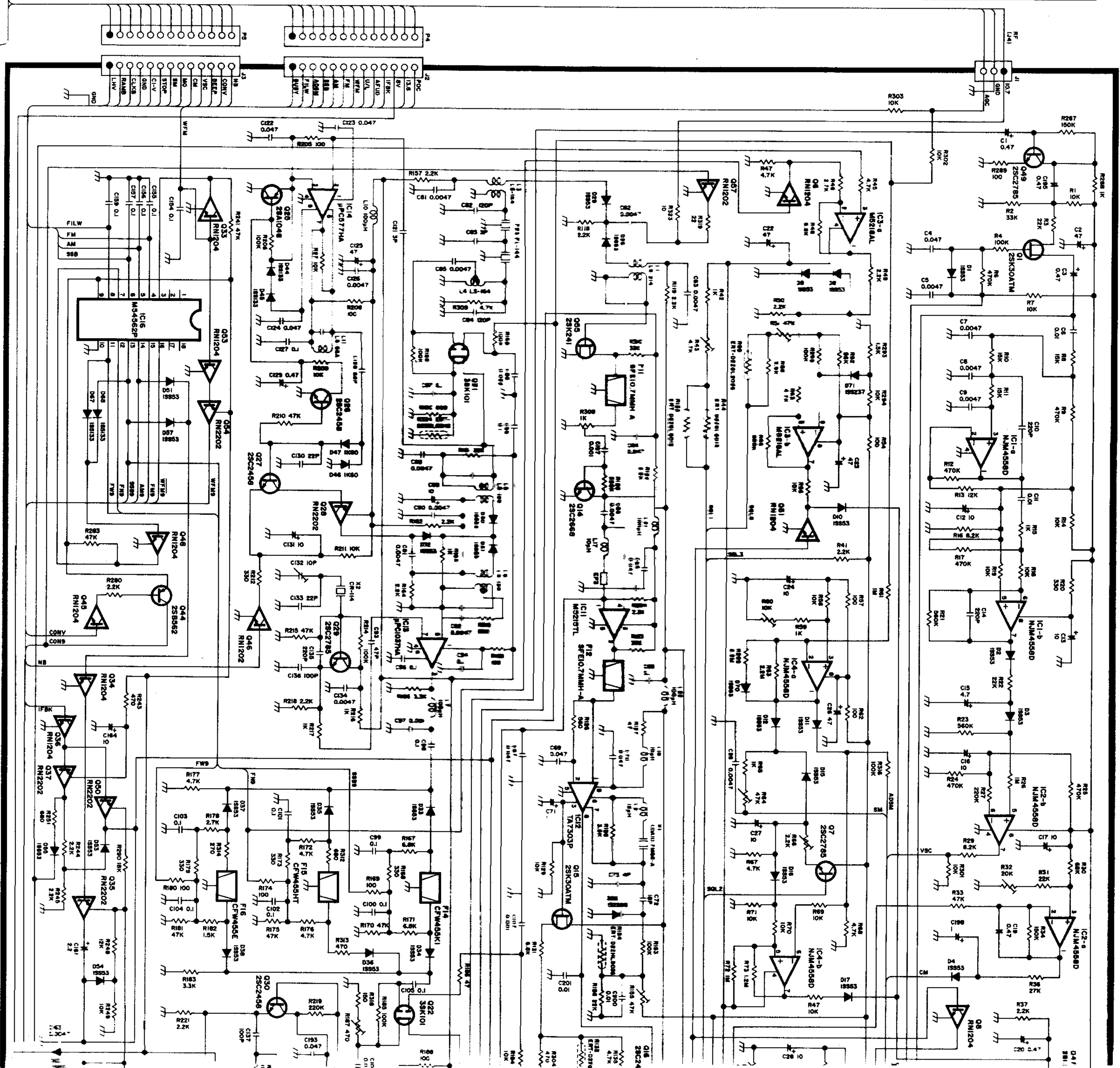
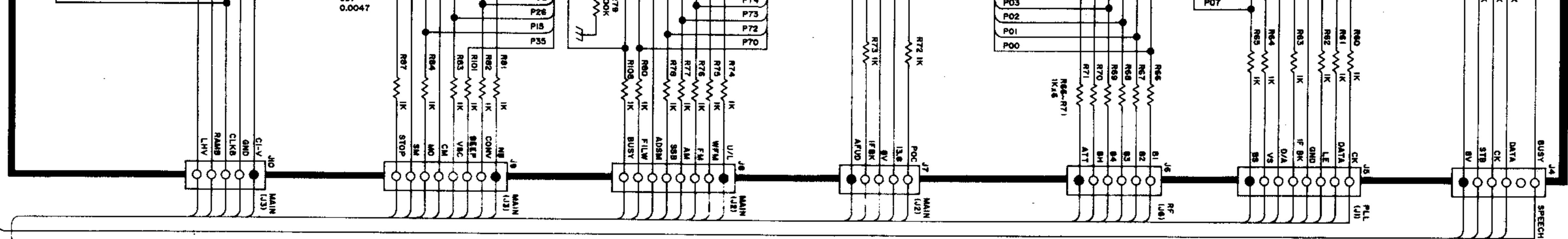
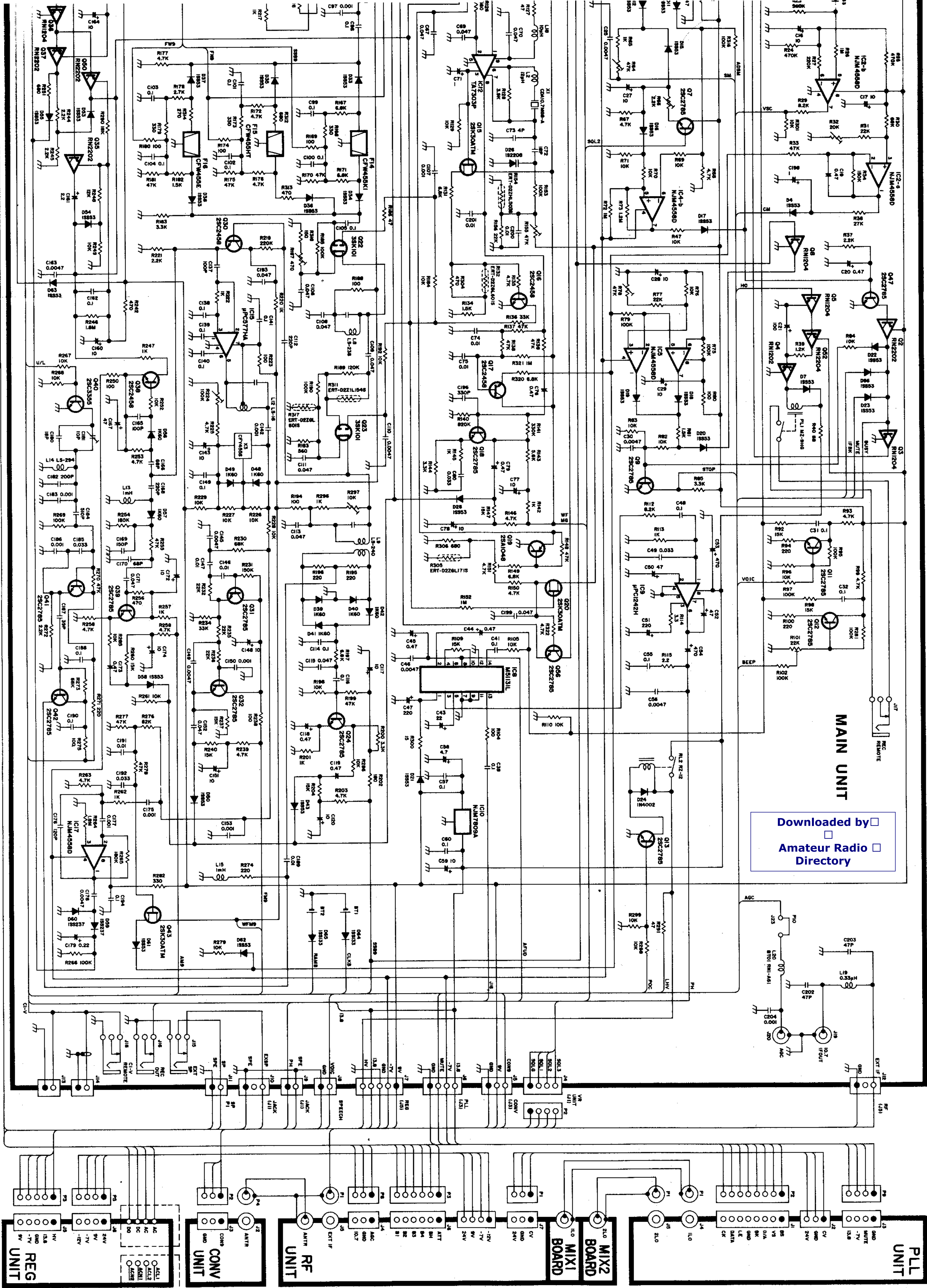










MAIN UNIT

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PLL UNIT

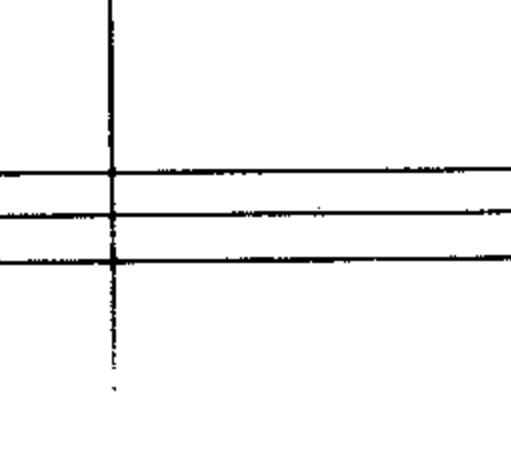
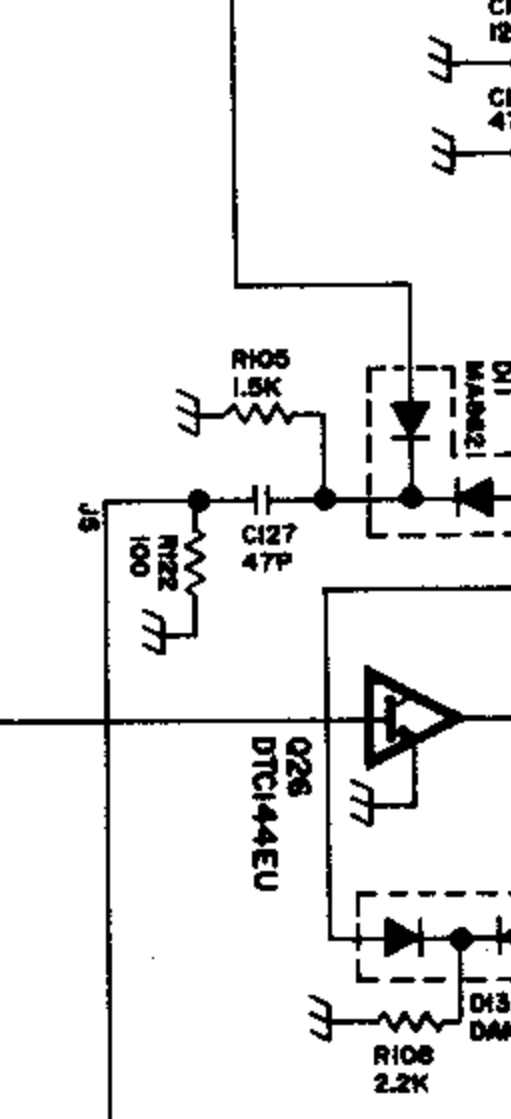
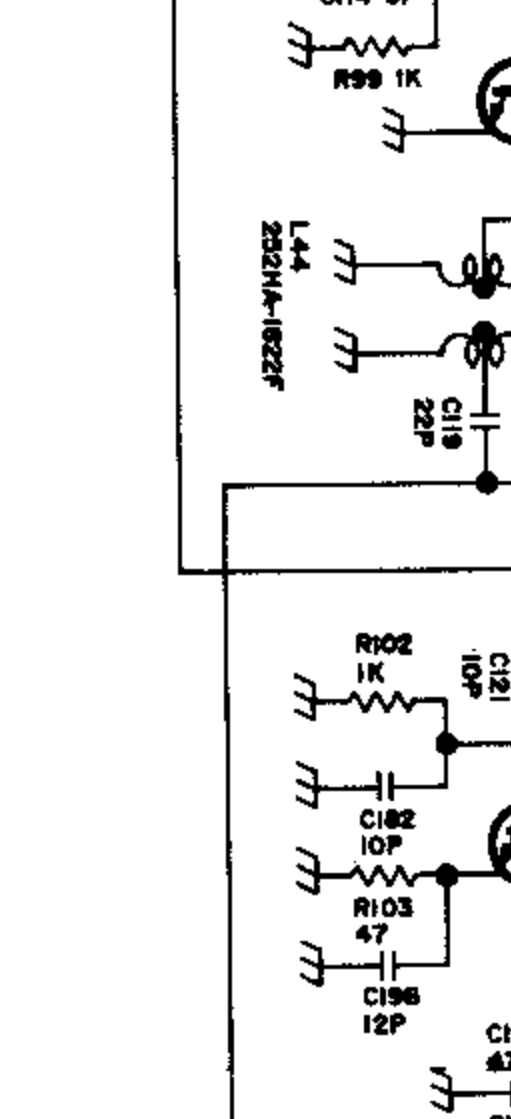
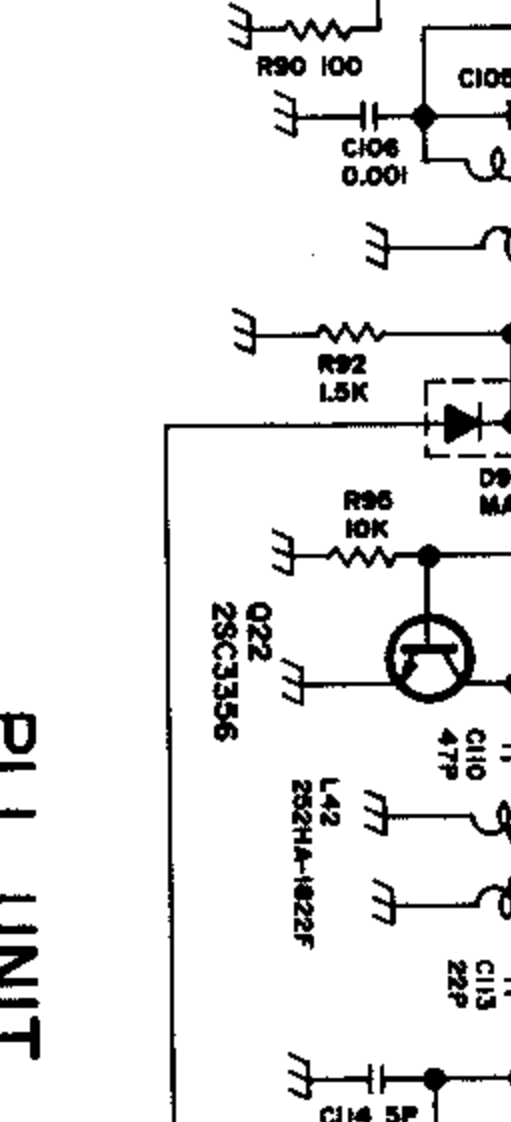
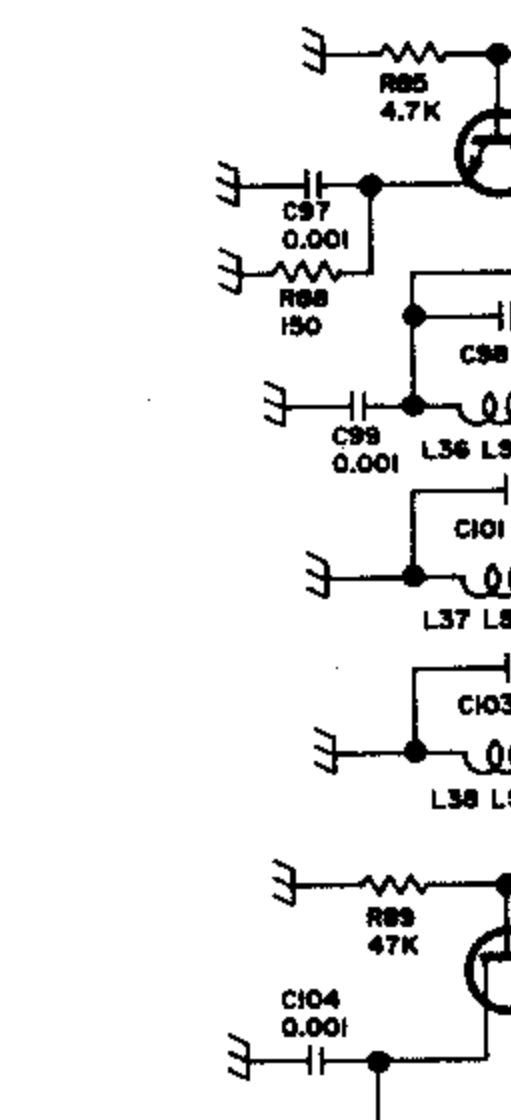
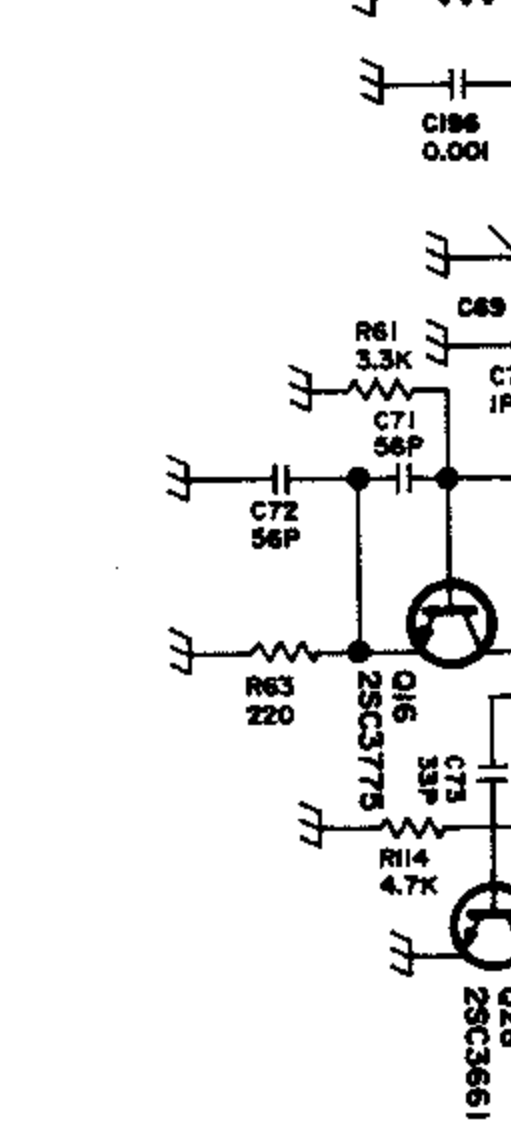
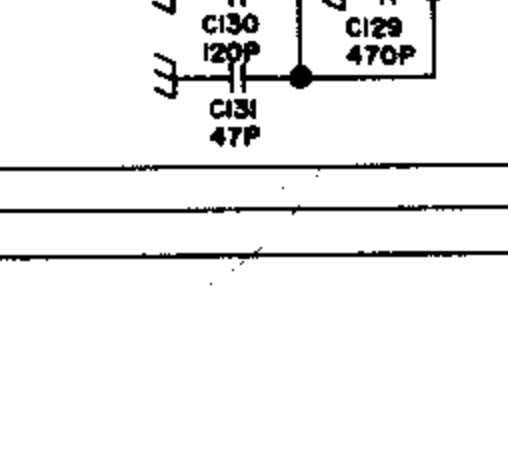
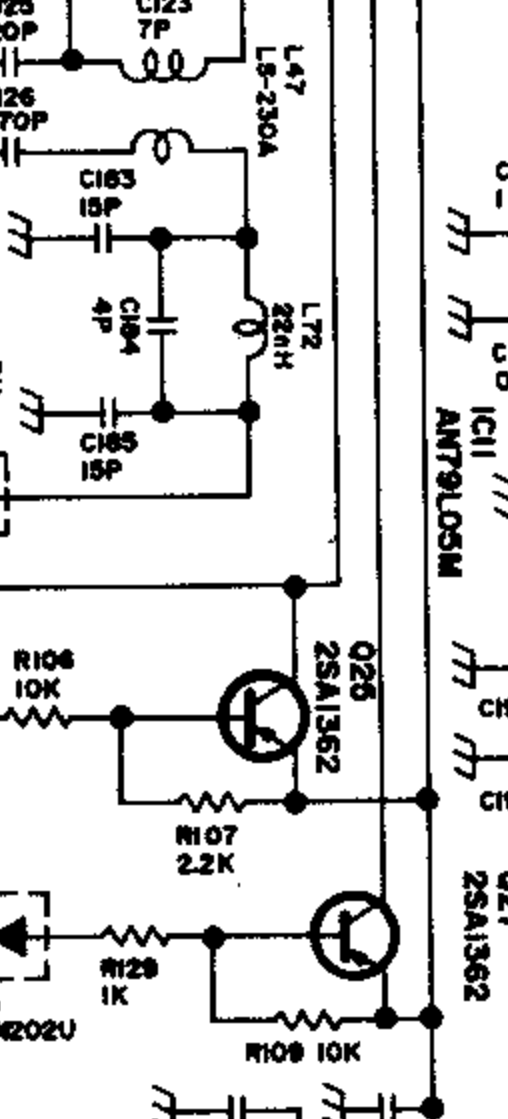
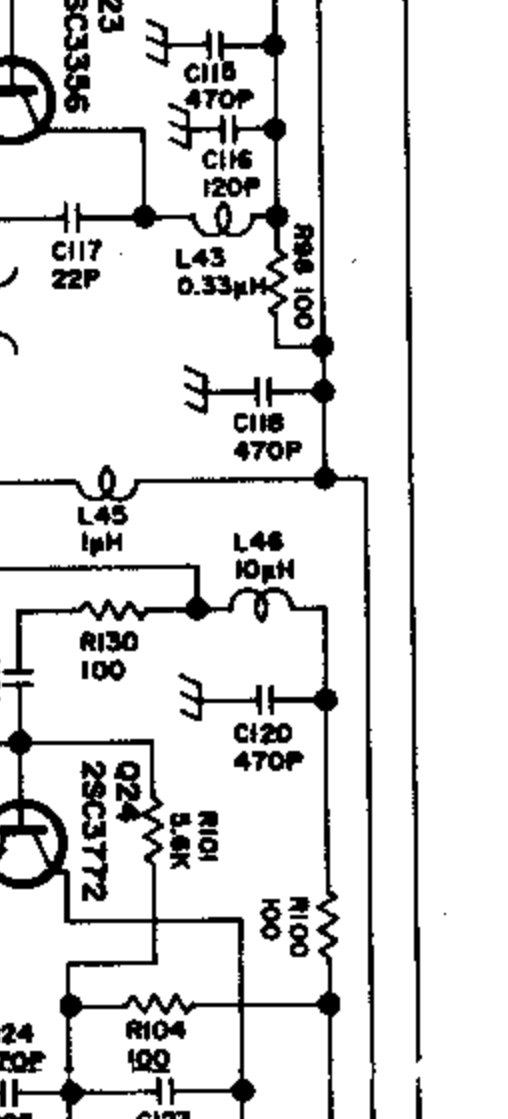
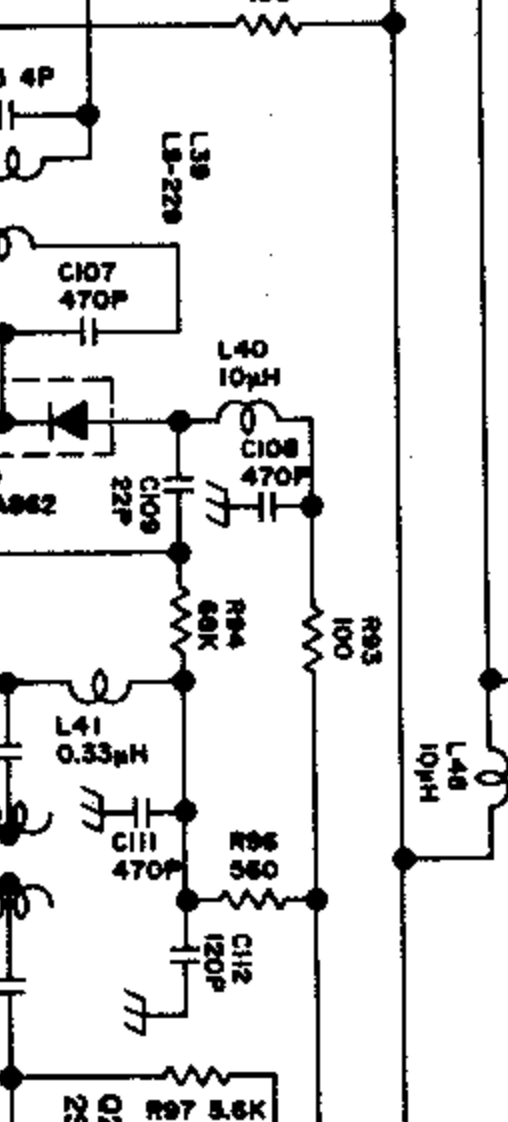
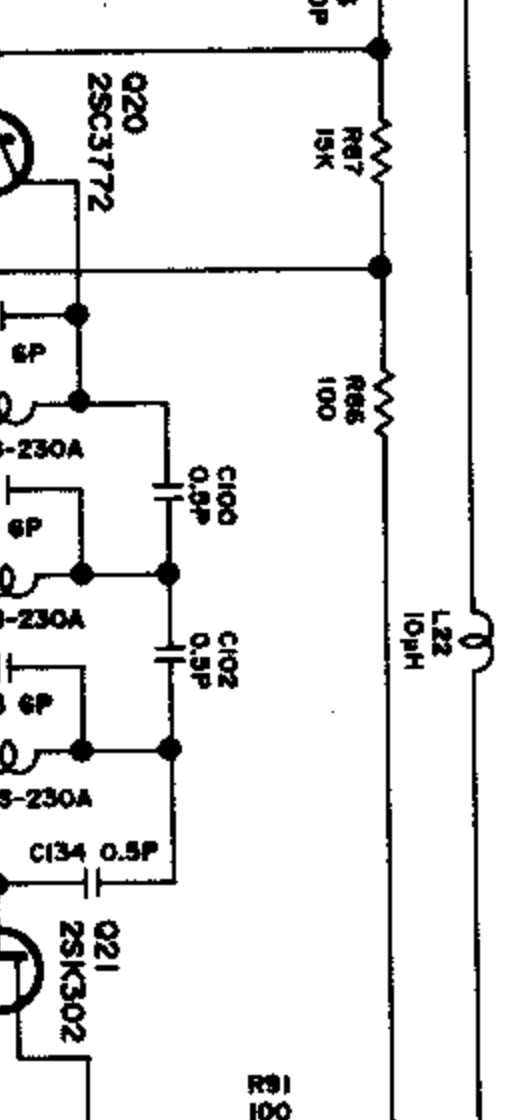
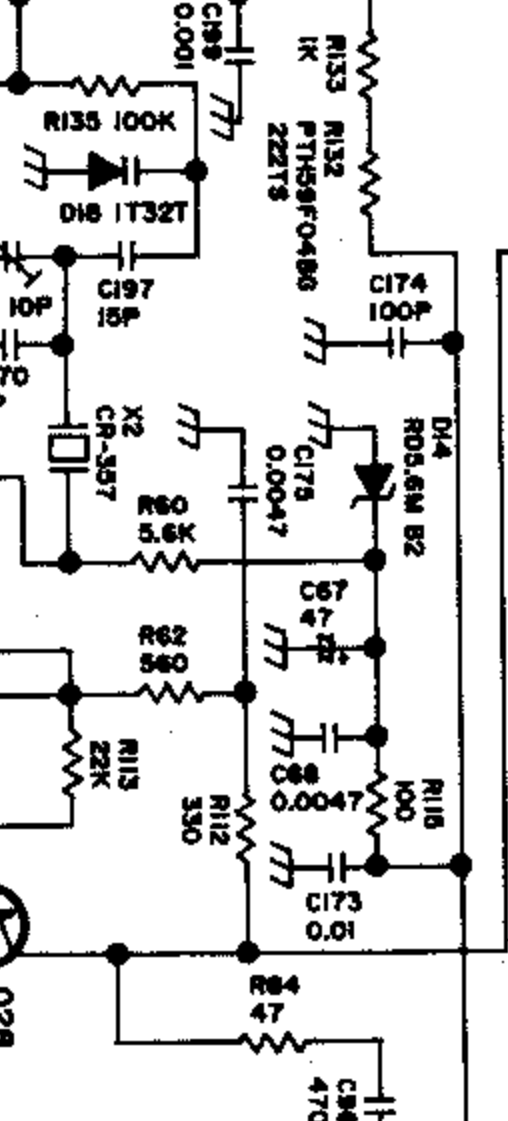
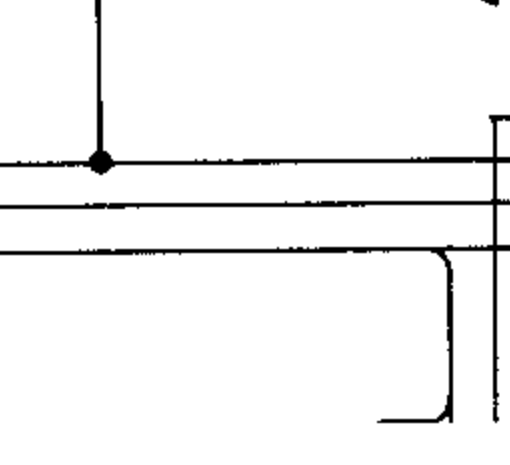
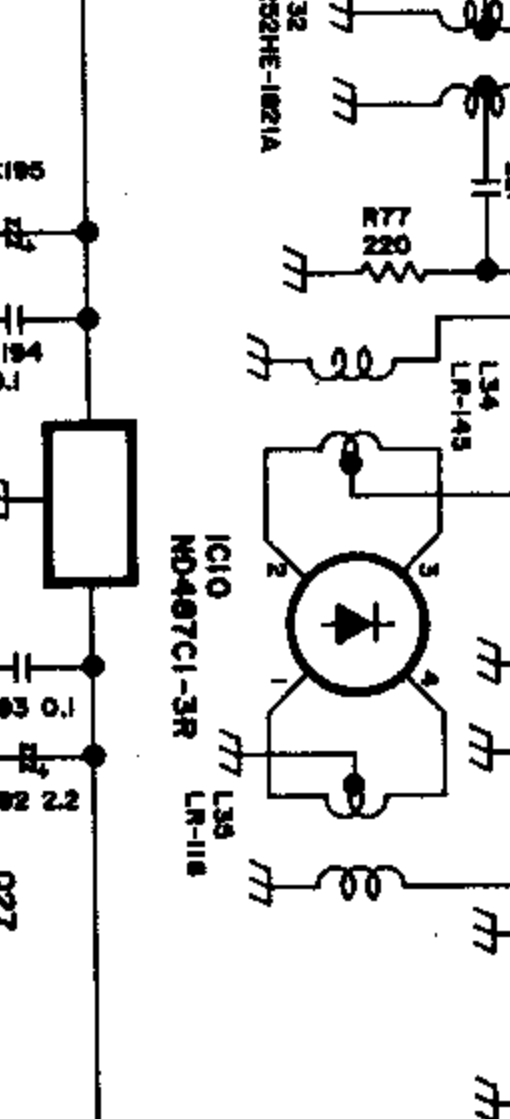
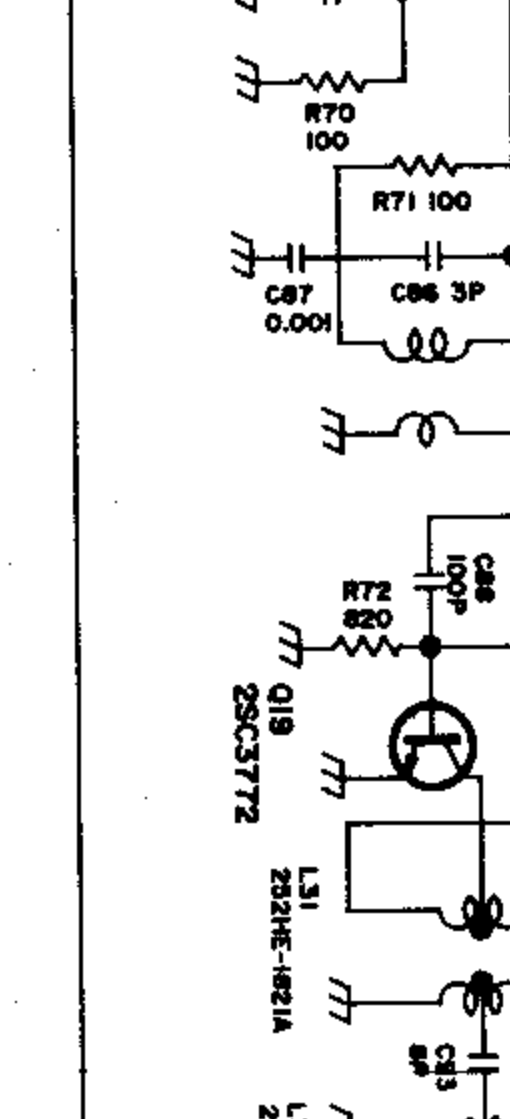
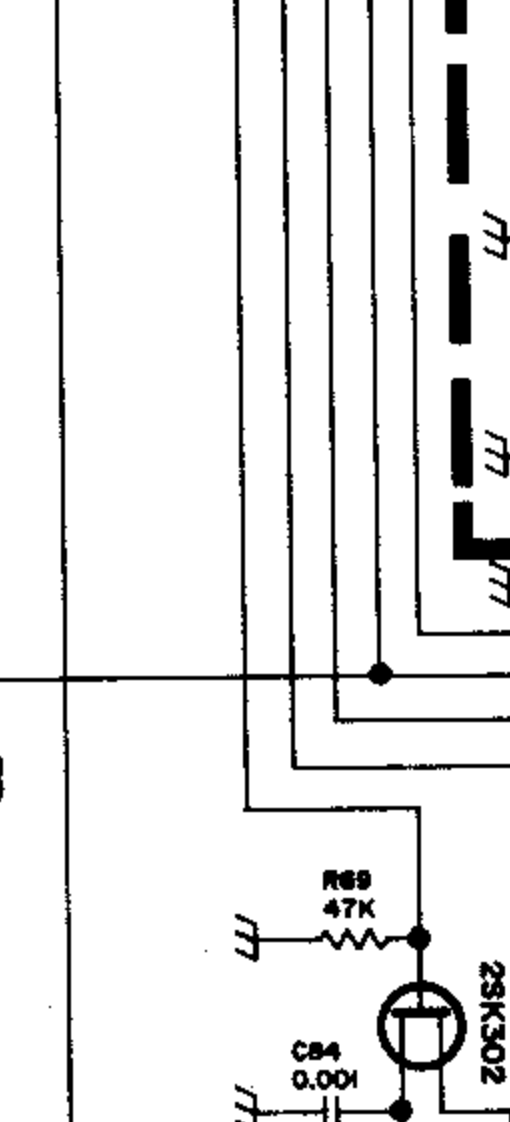
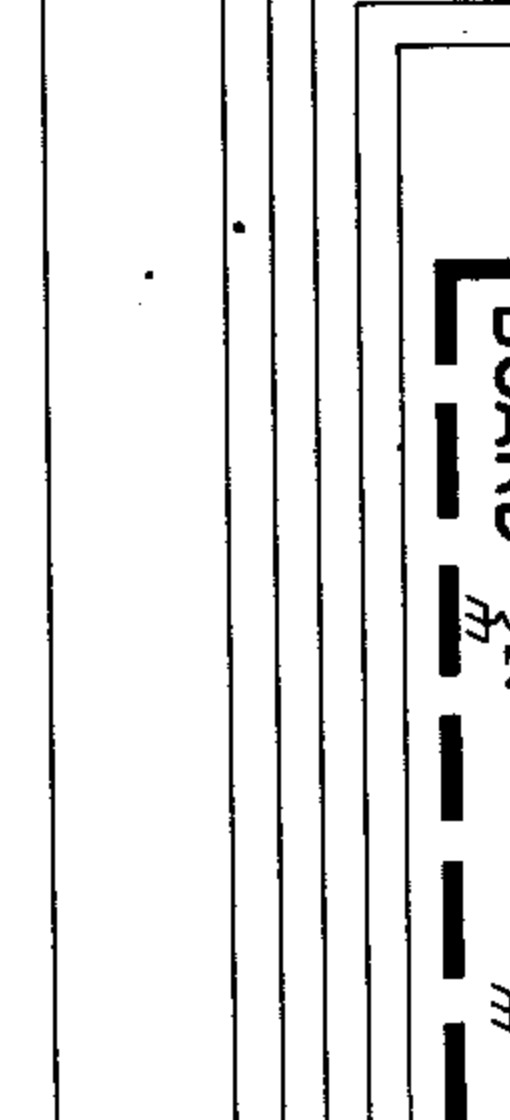
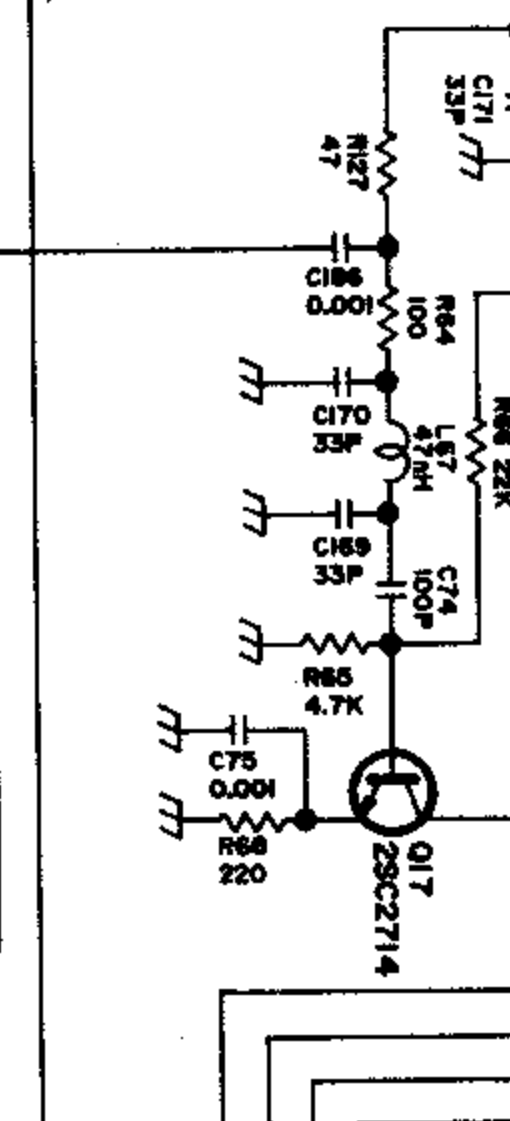
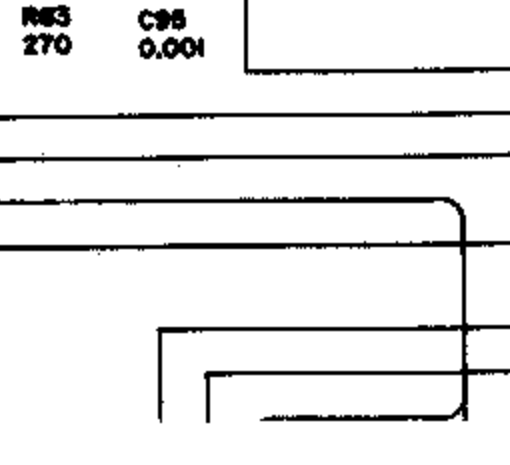
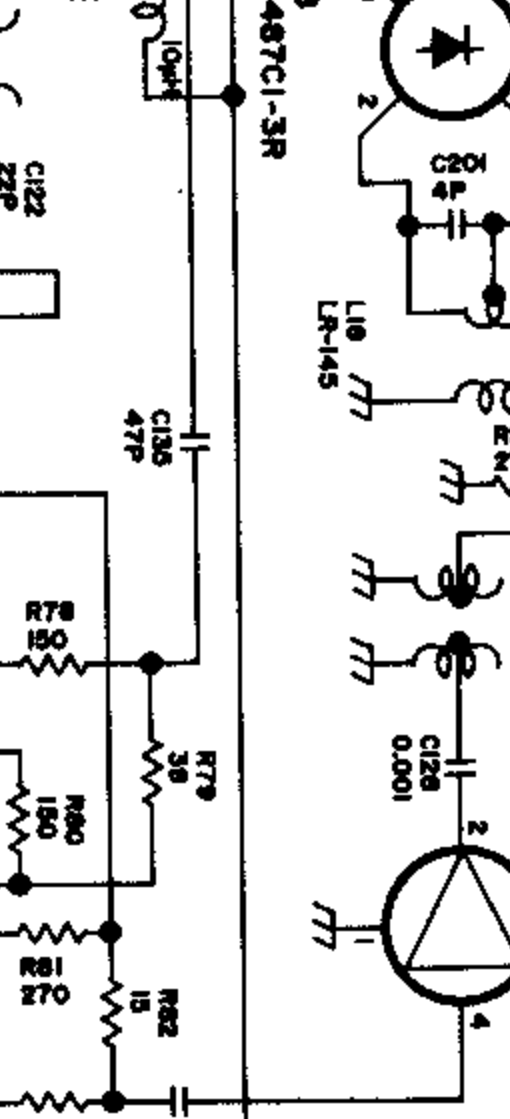
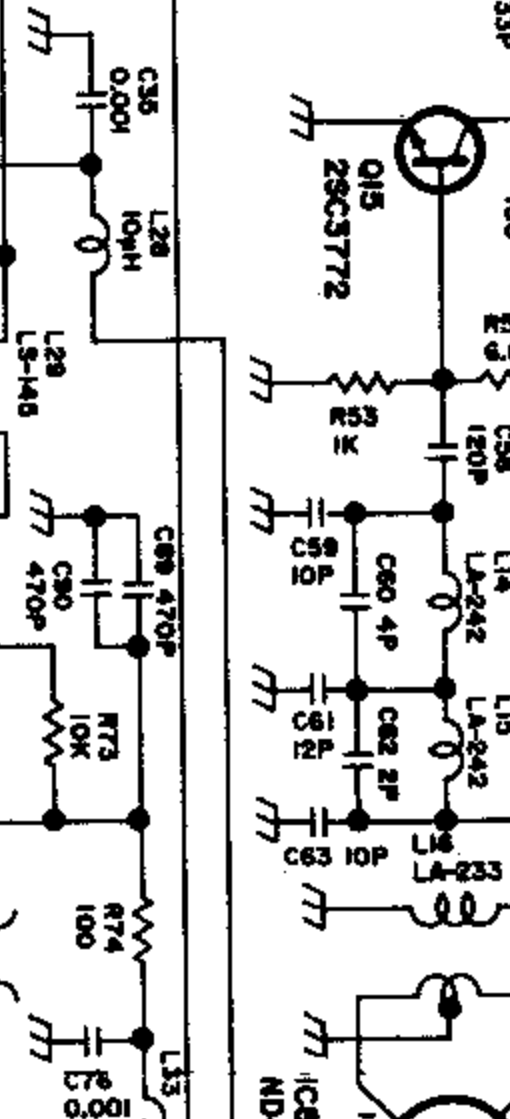
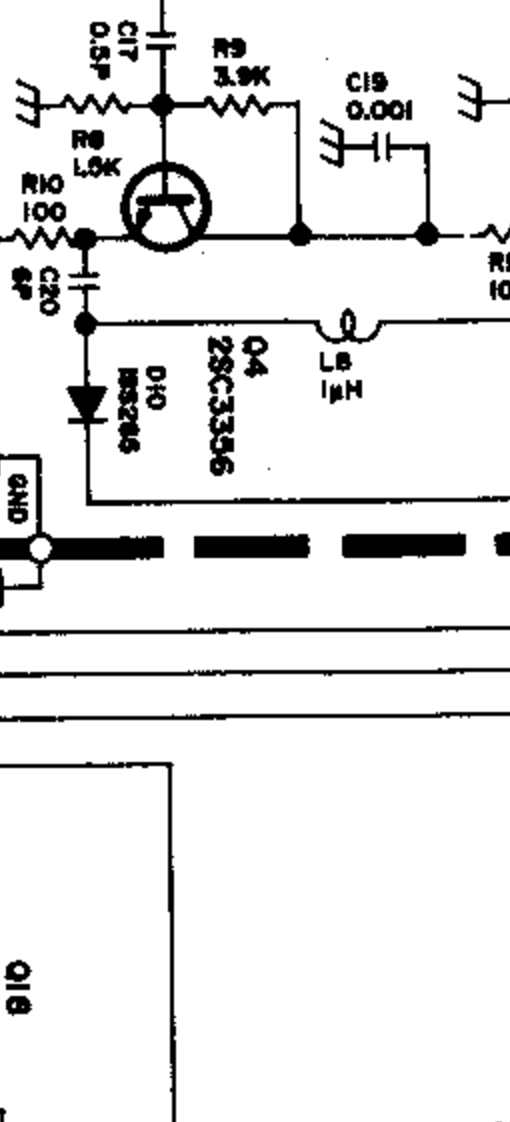
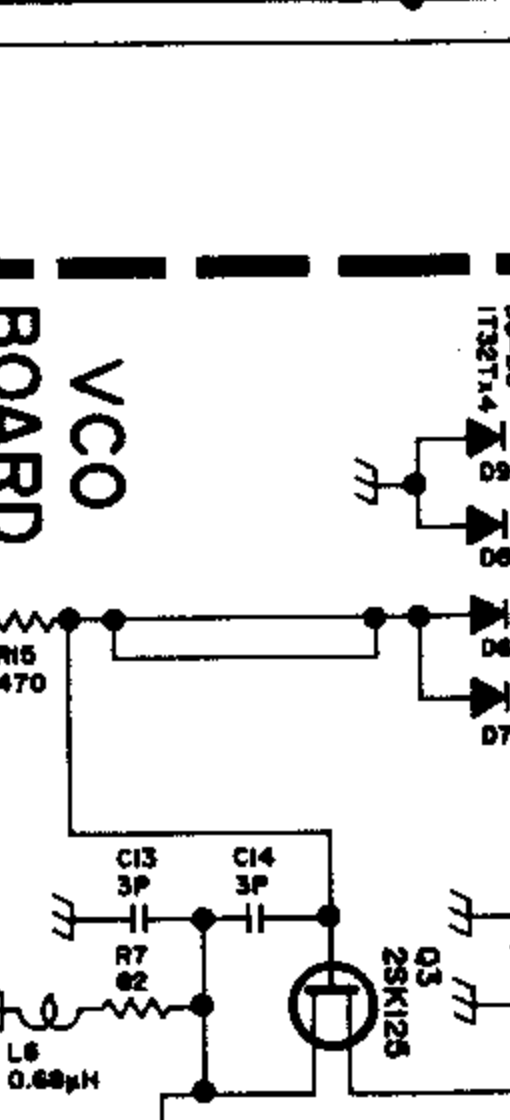
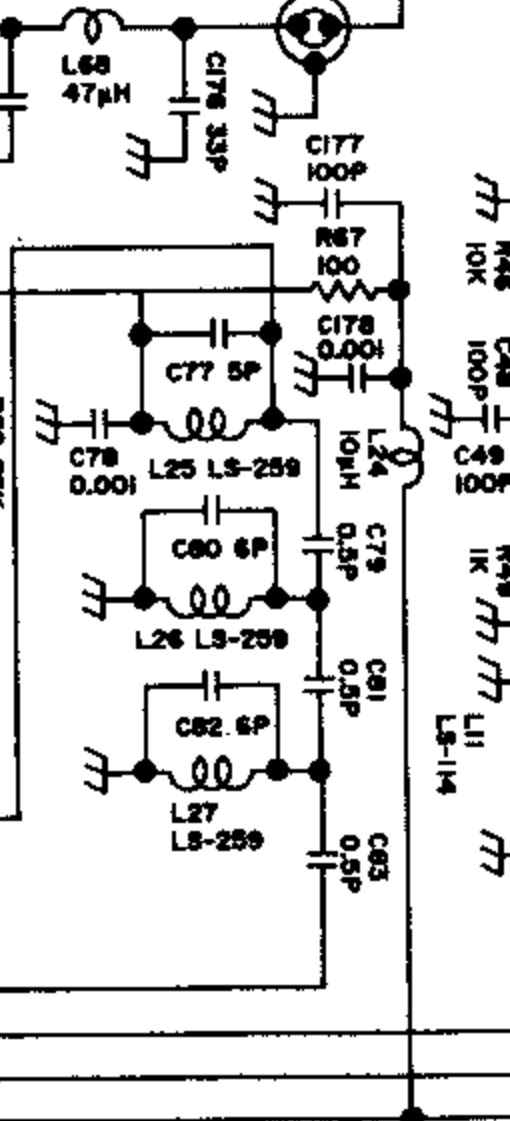
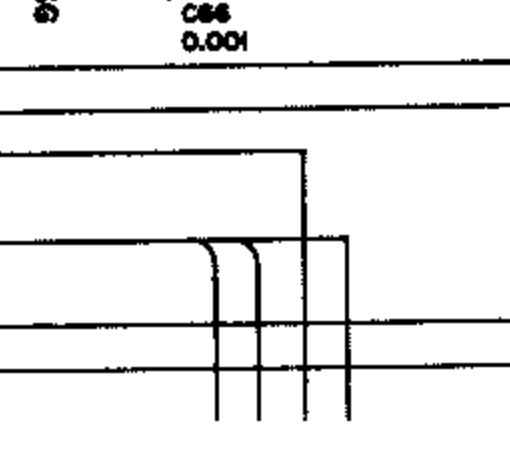
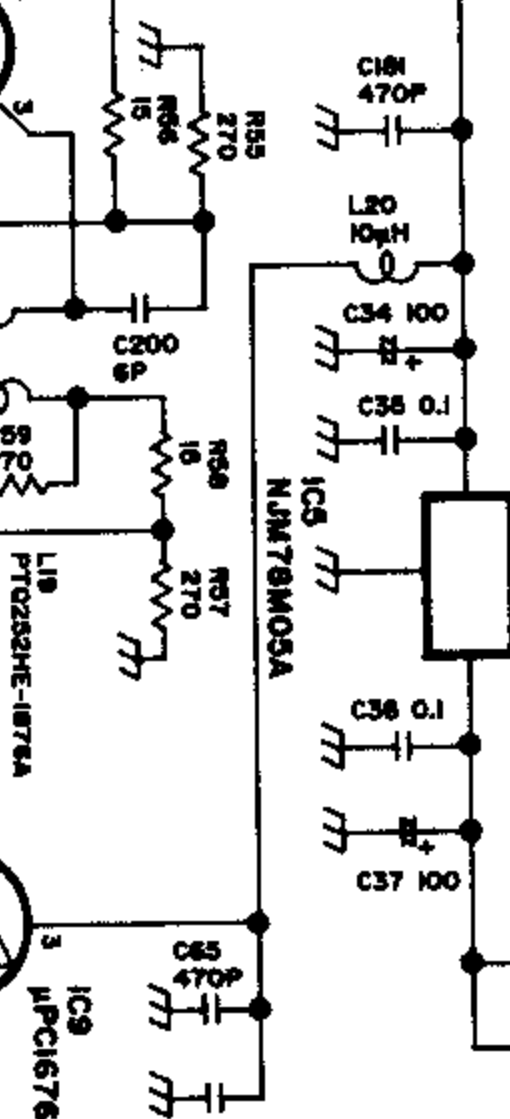
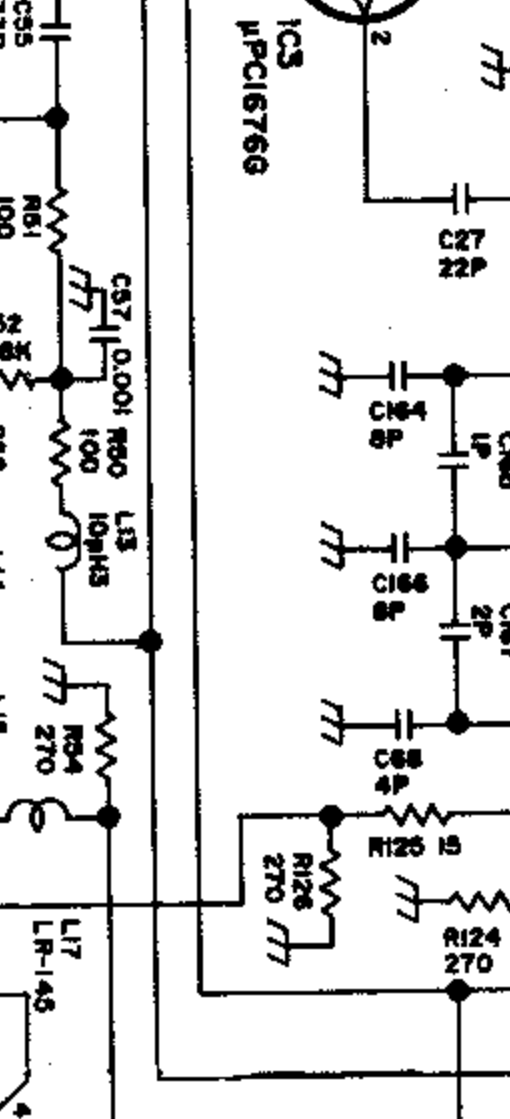
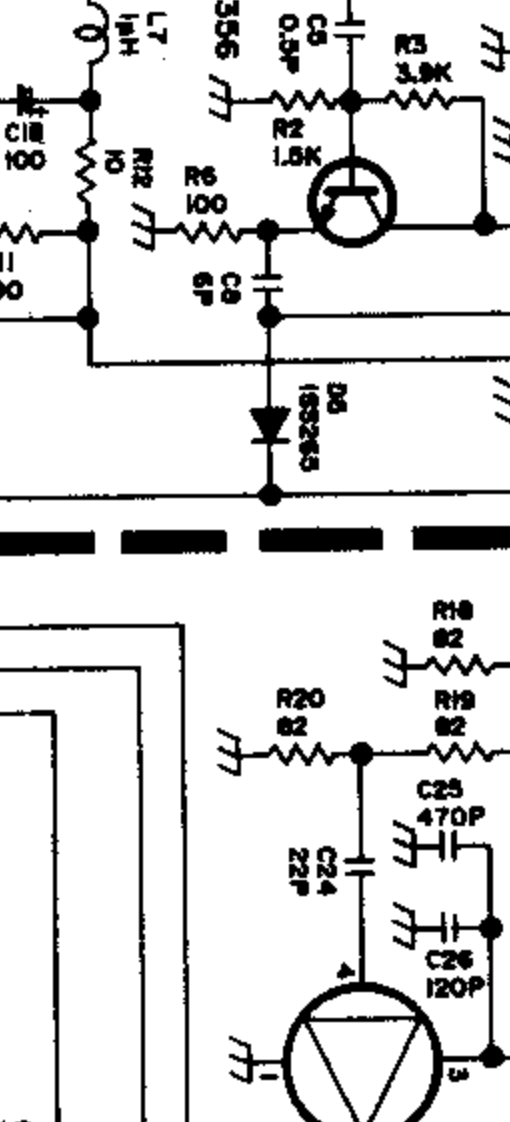
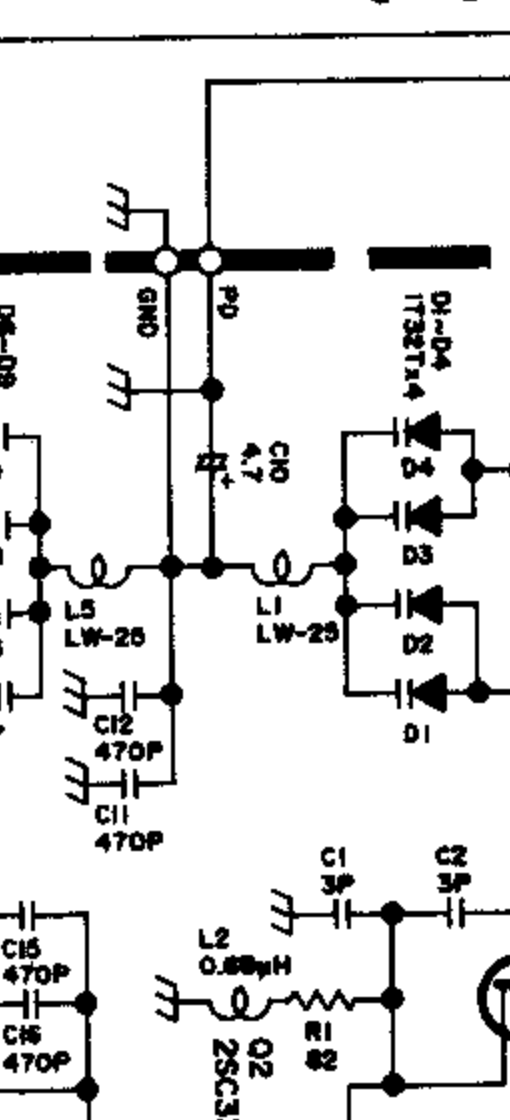
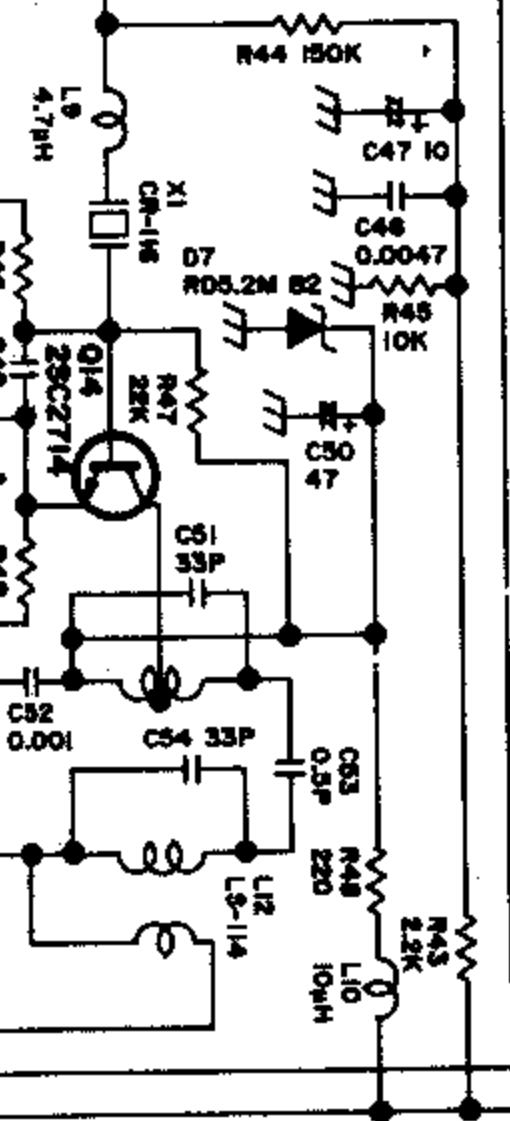
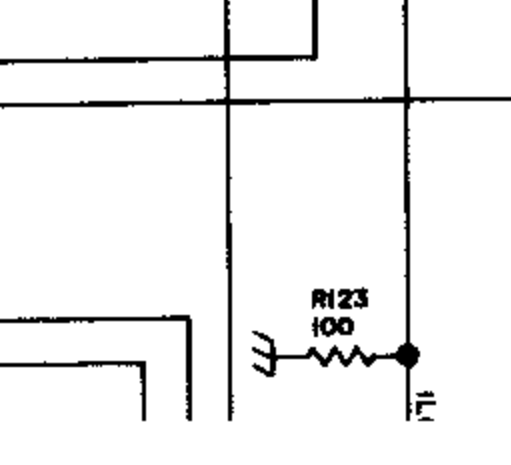
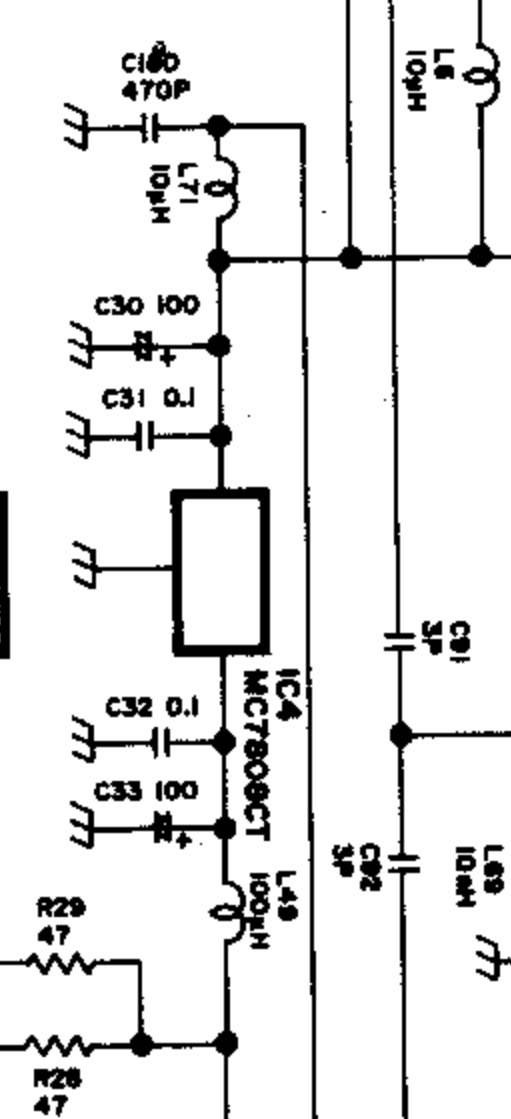
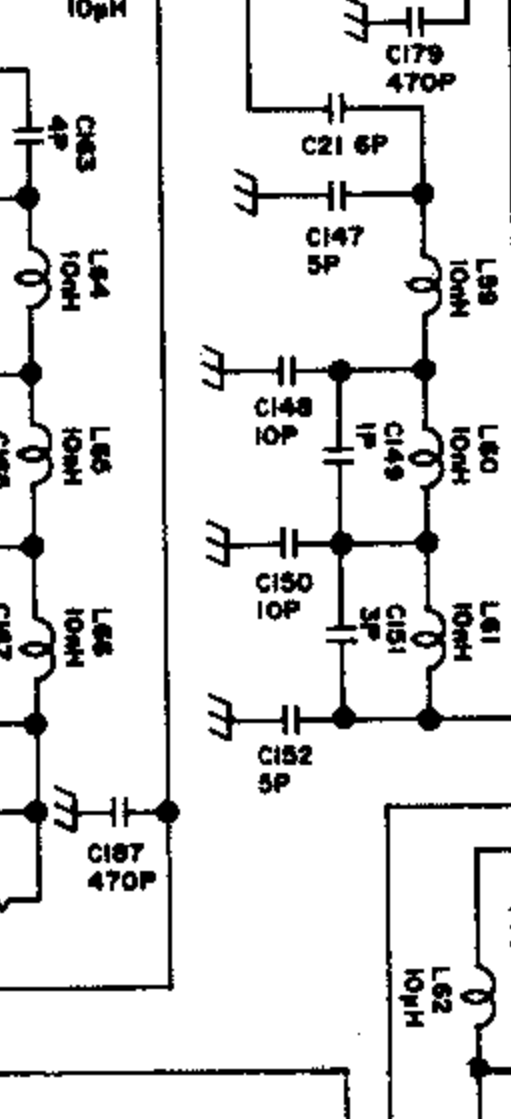
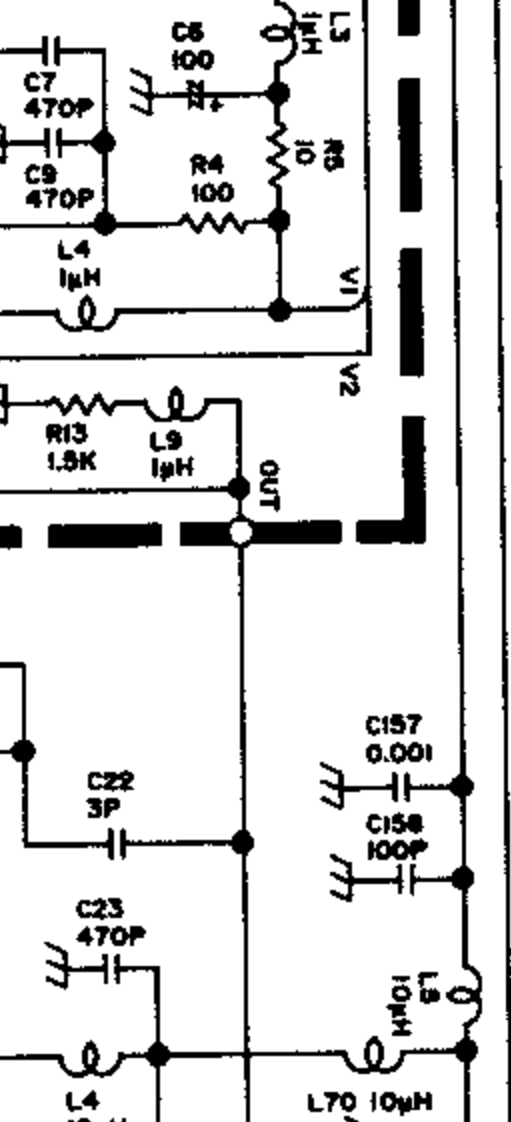
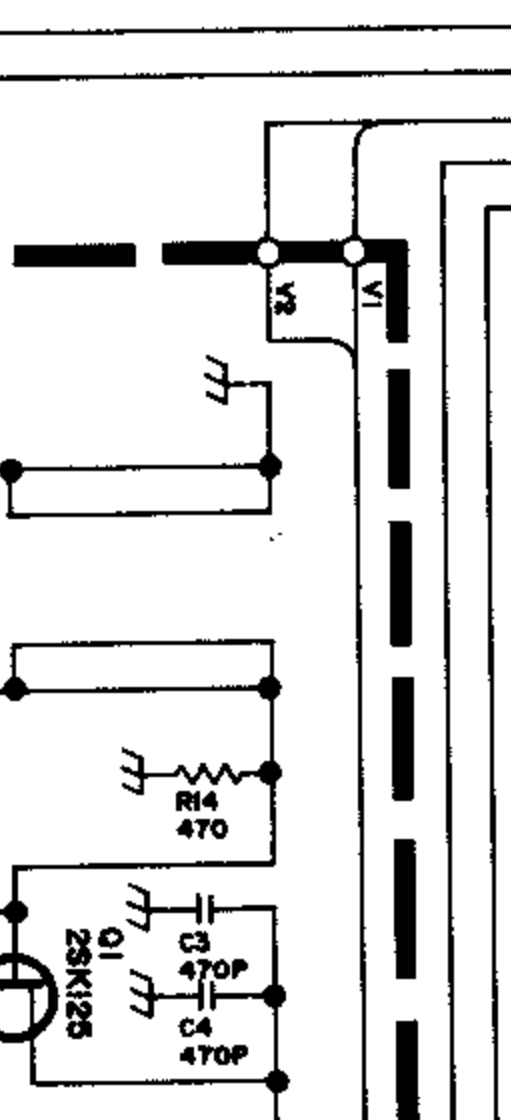
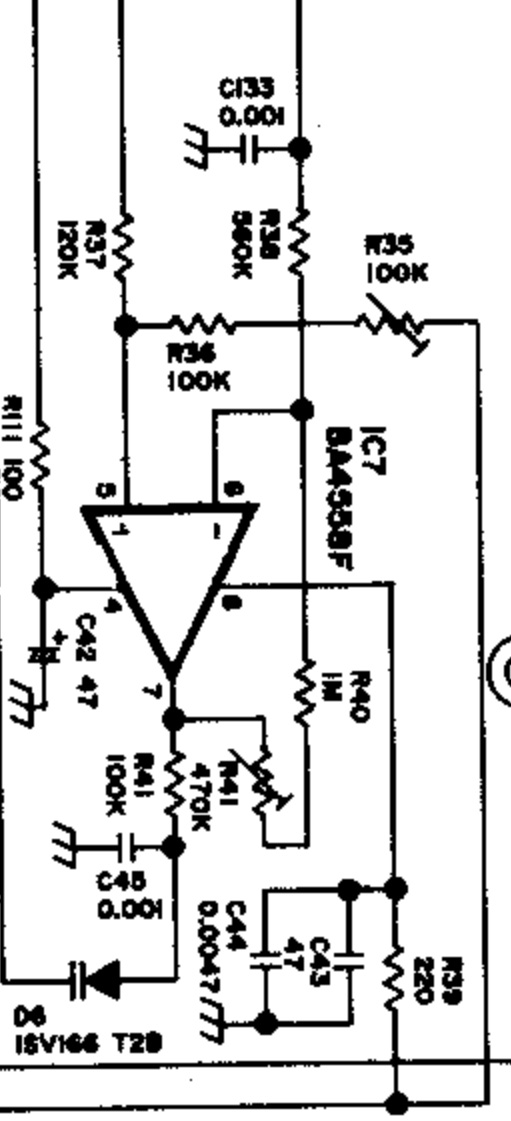
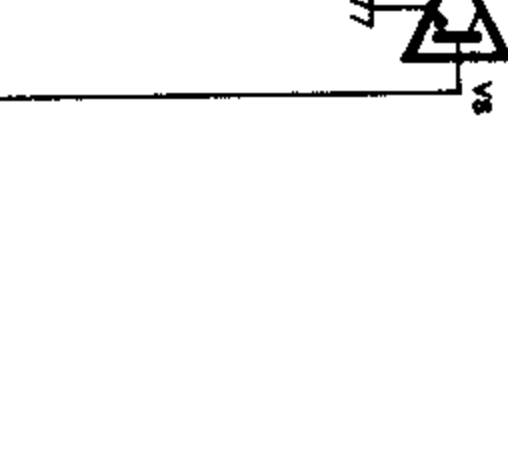
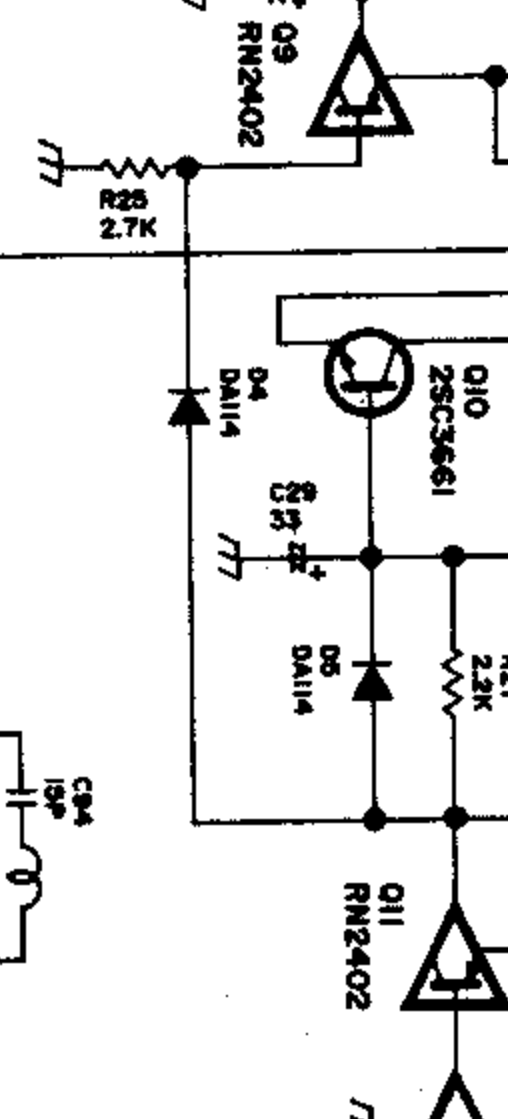
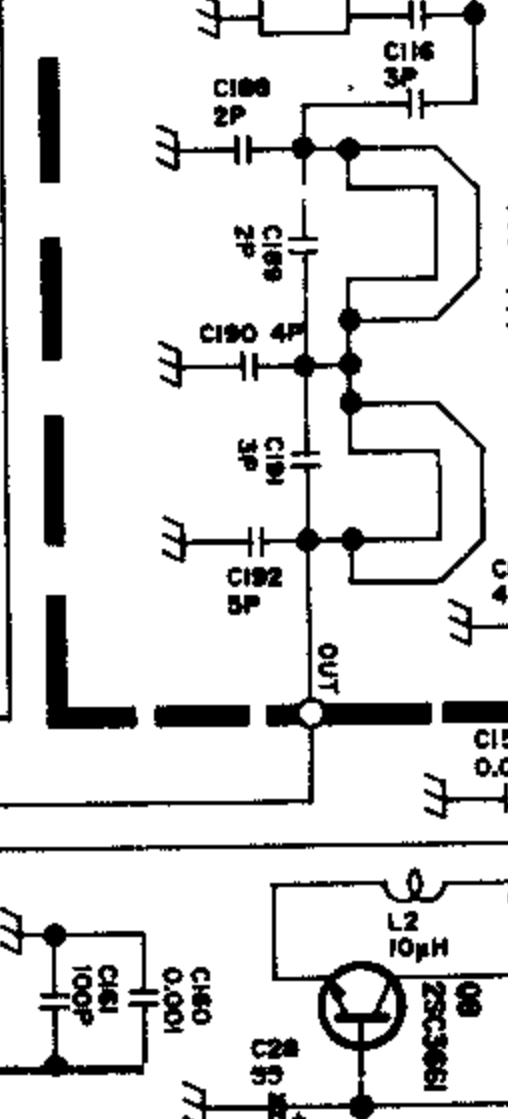
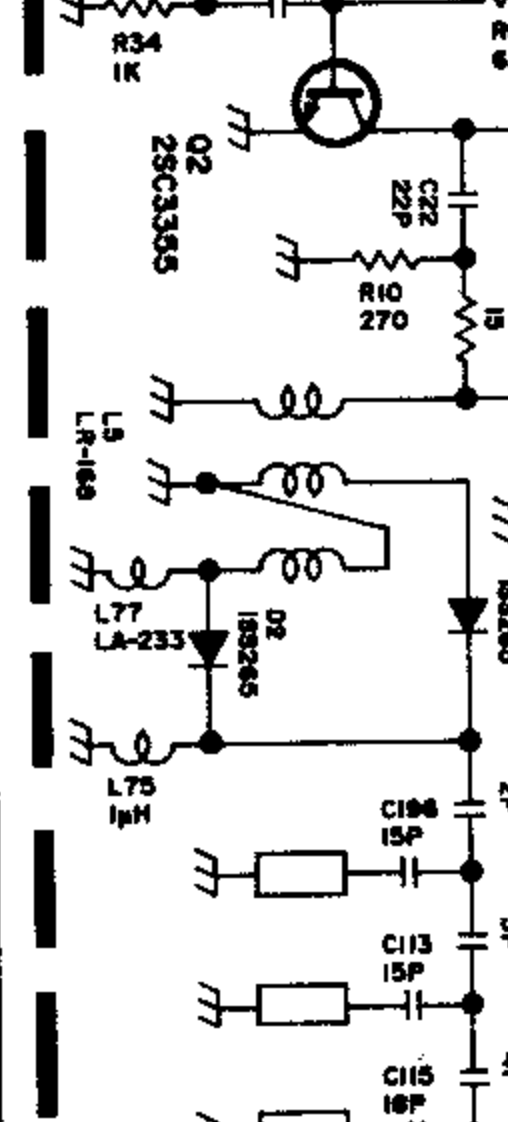
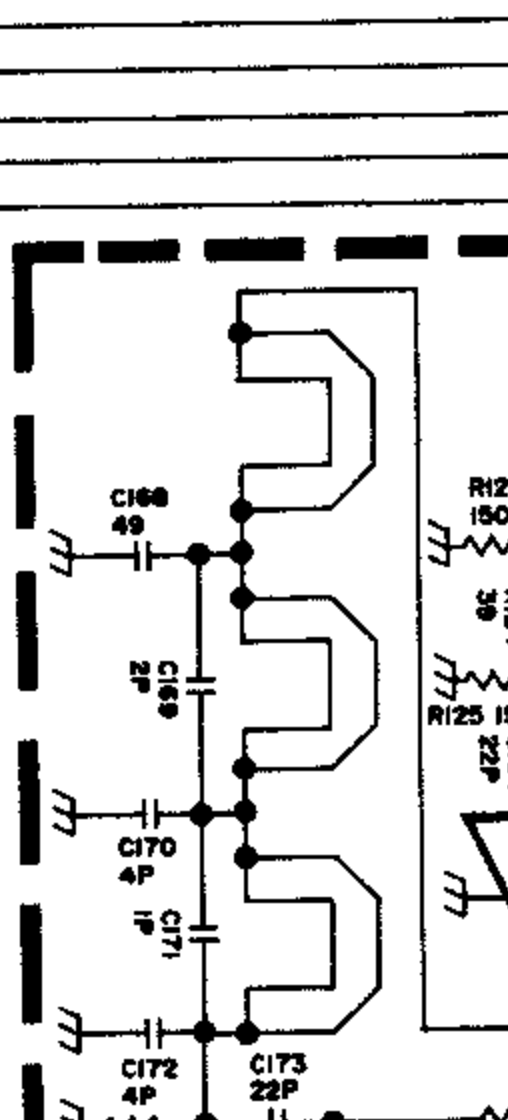
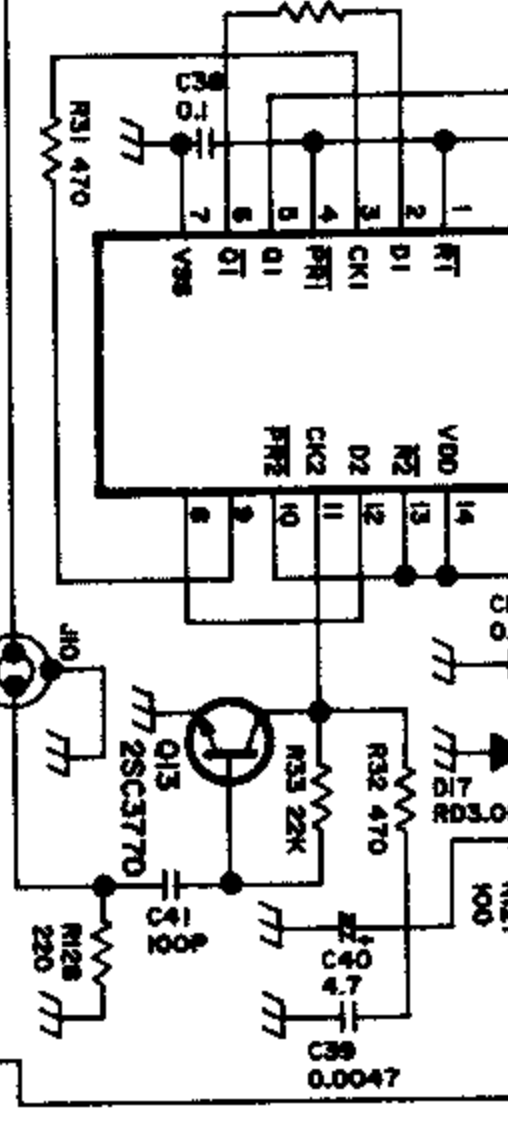
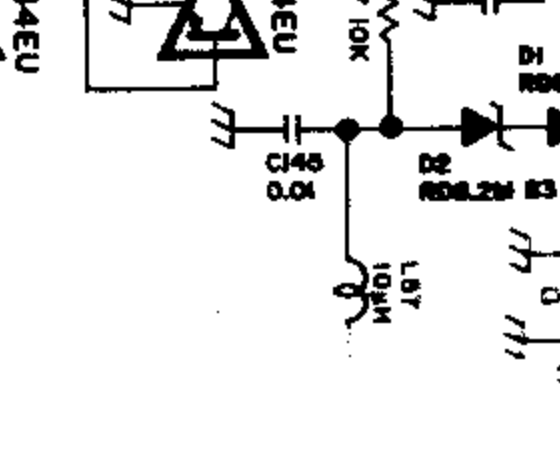
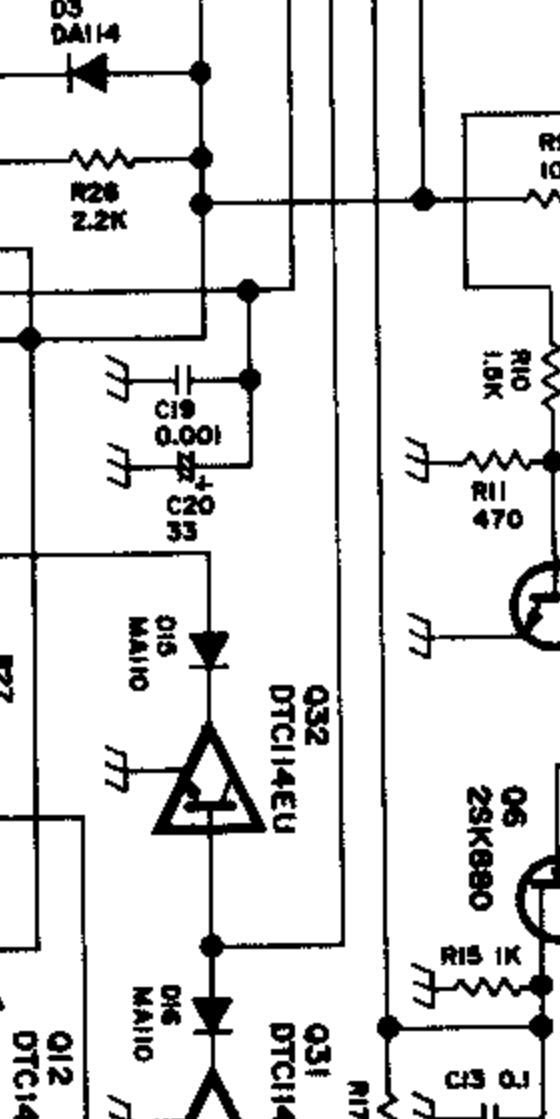
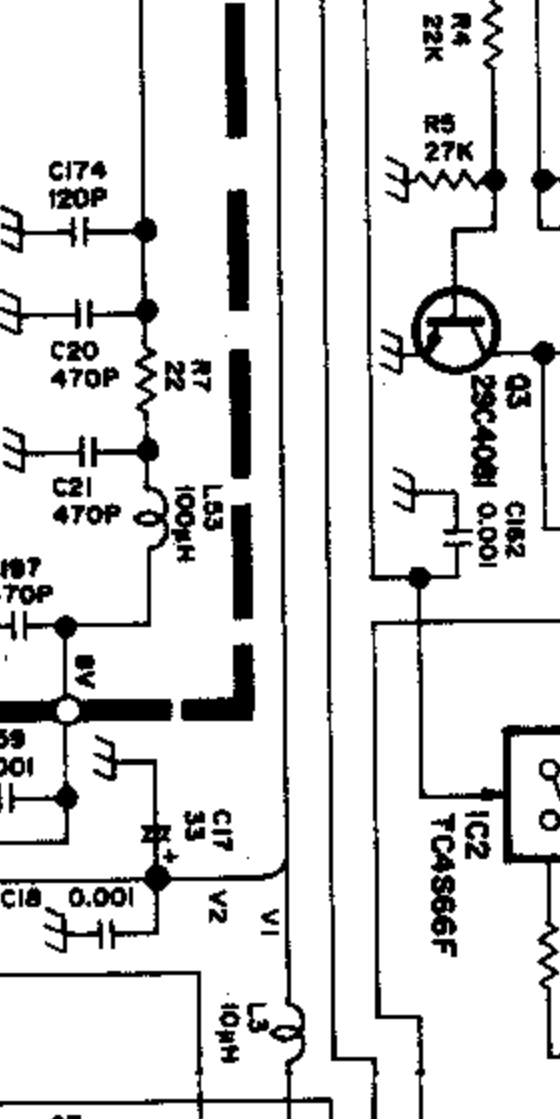
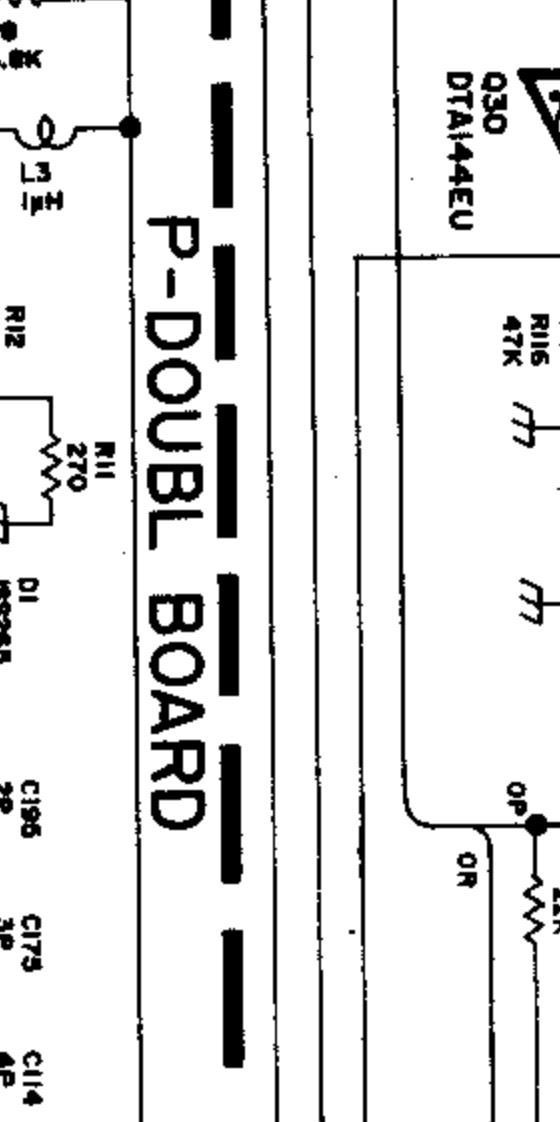
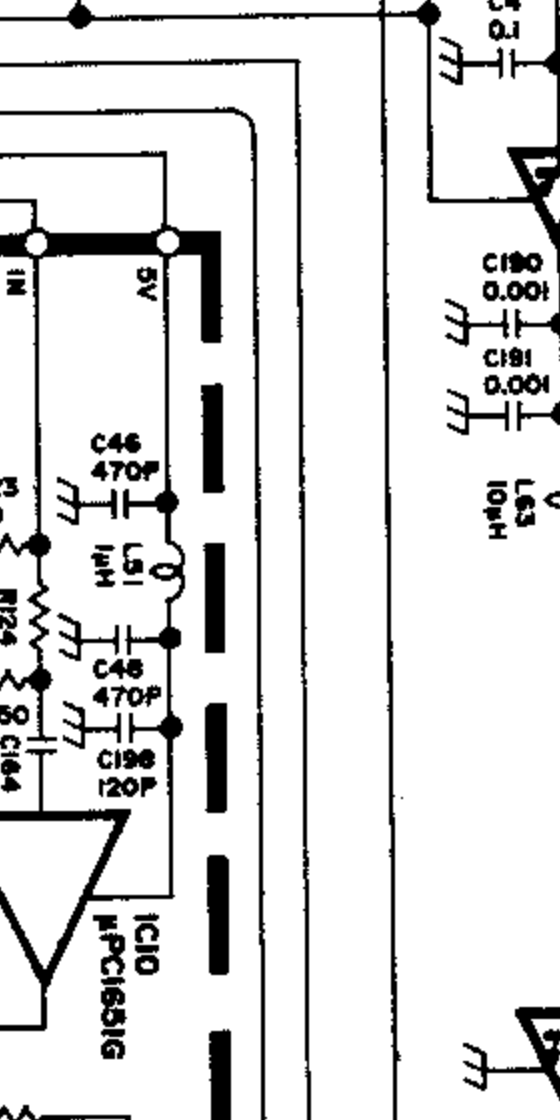
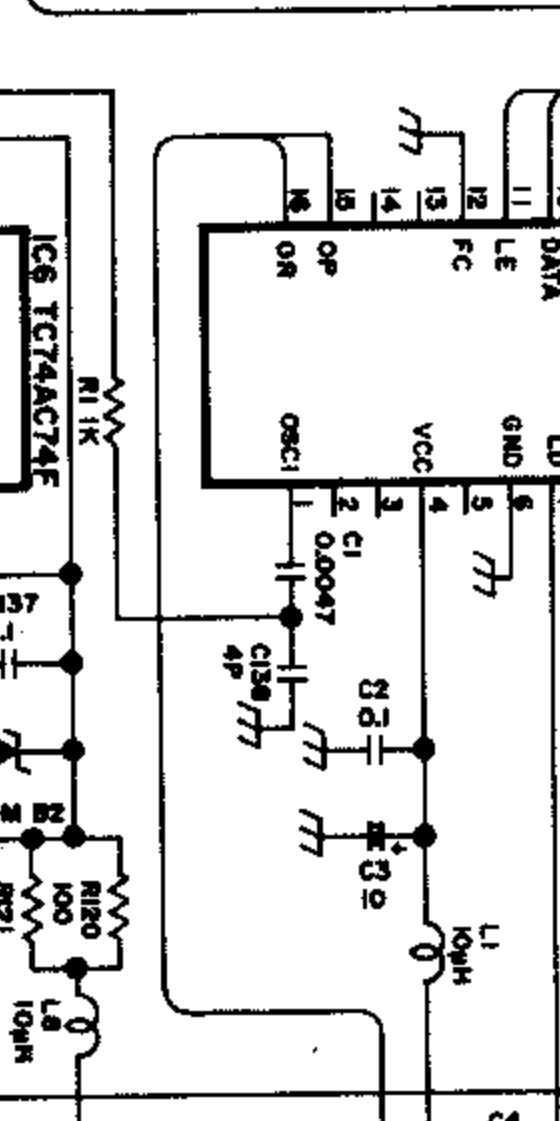
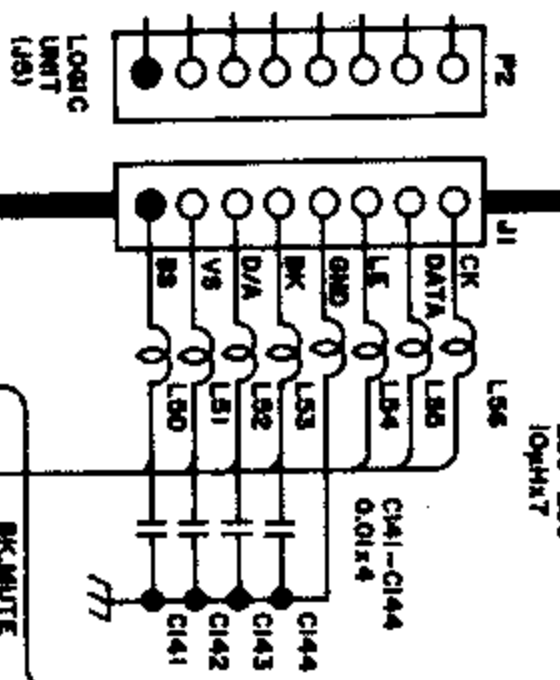
MIX2 BOARD

MIX1 BOARD

RF UNIT

CONV UNIT

REG UNIT

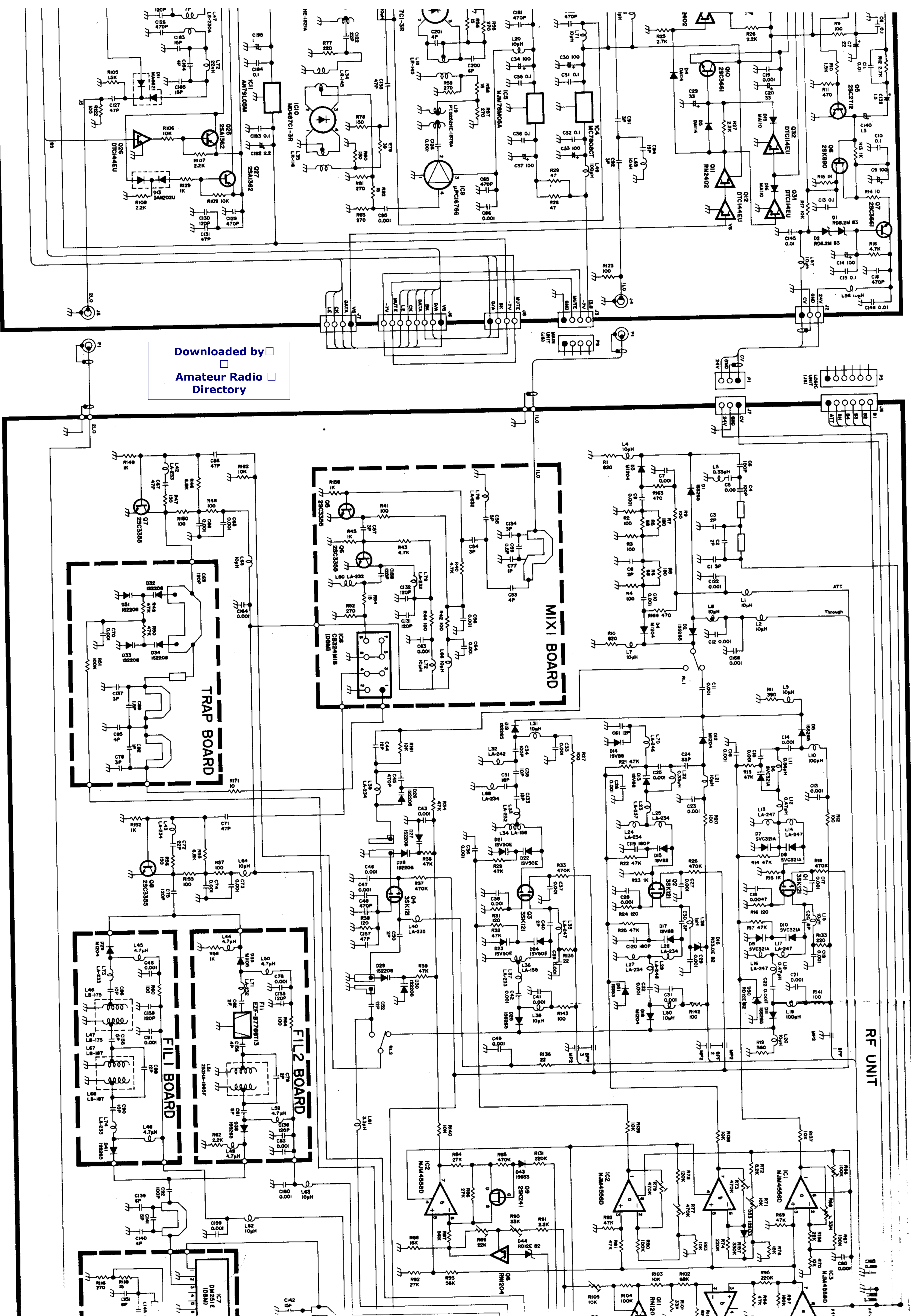


P-DOUBL BOARD

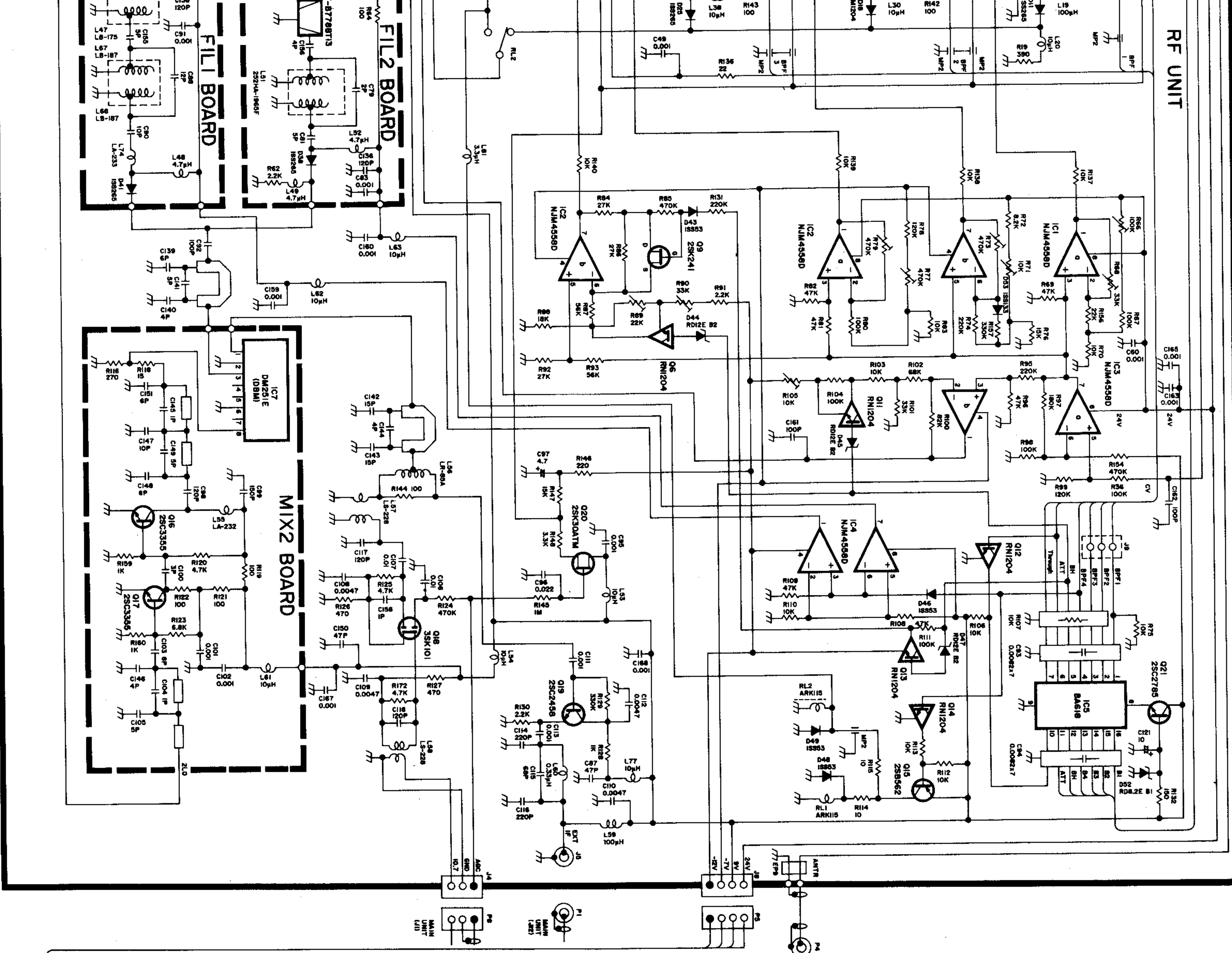
VCO BOARD

PLL UNIT

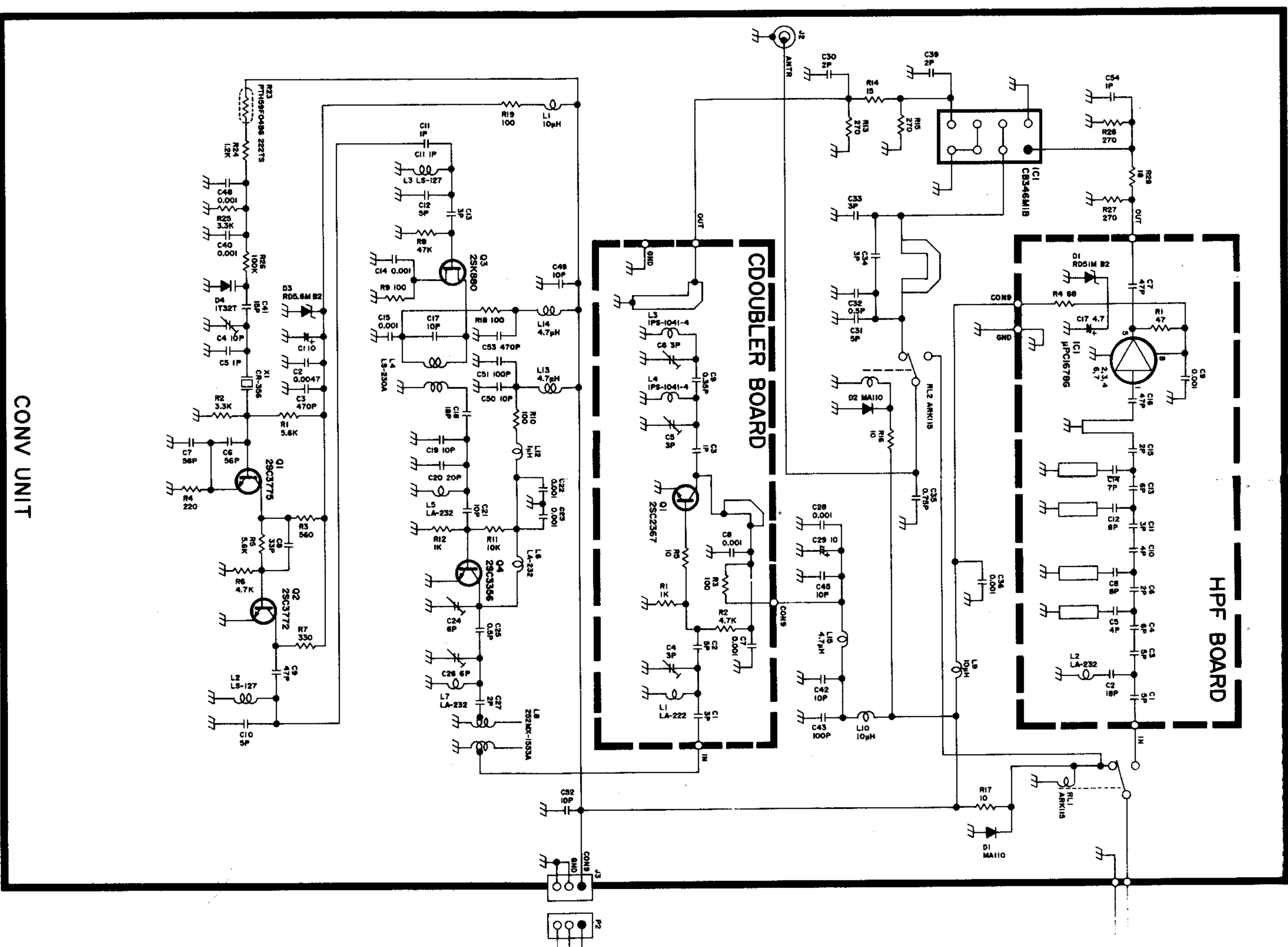
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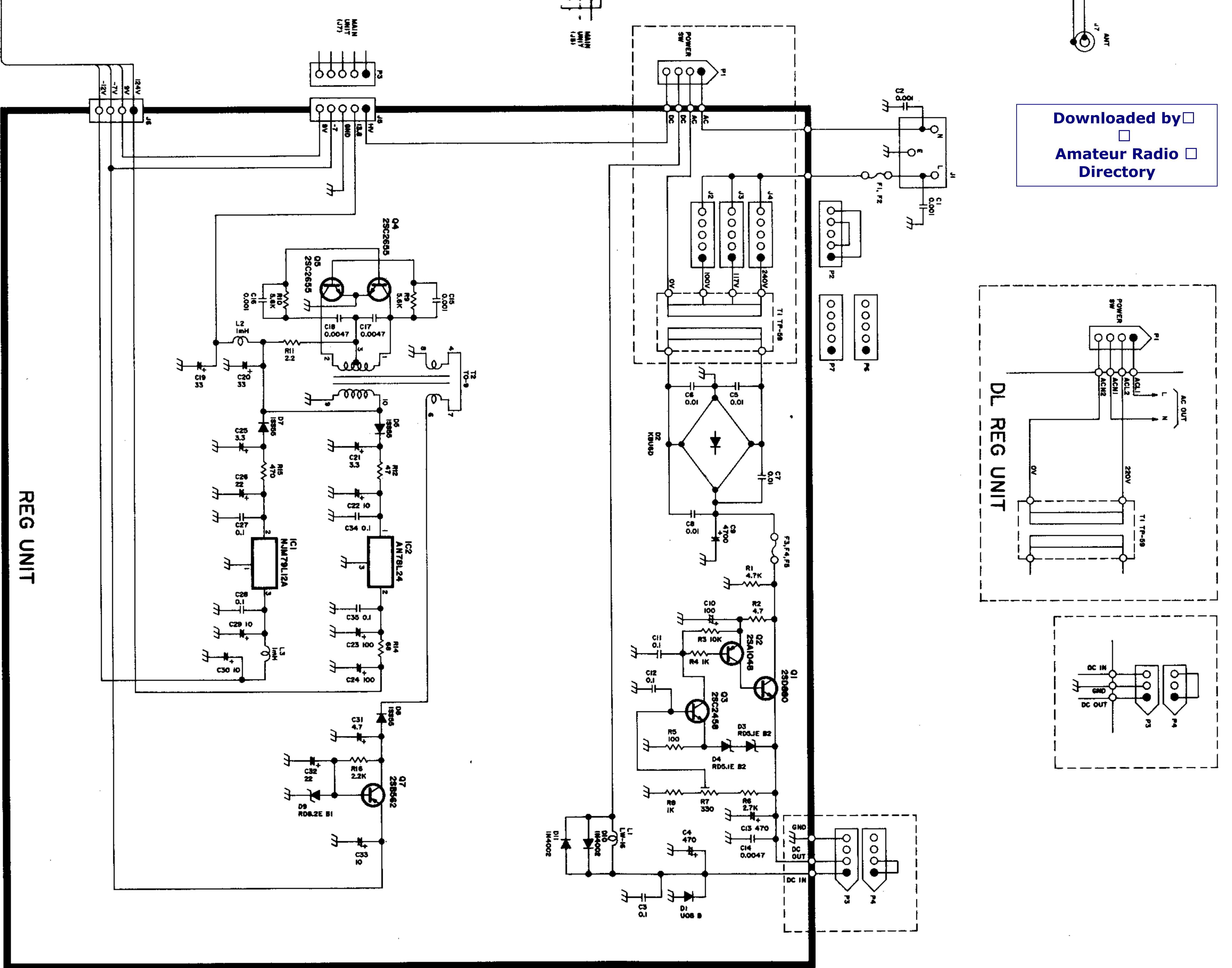
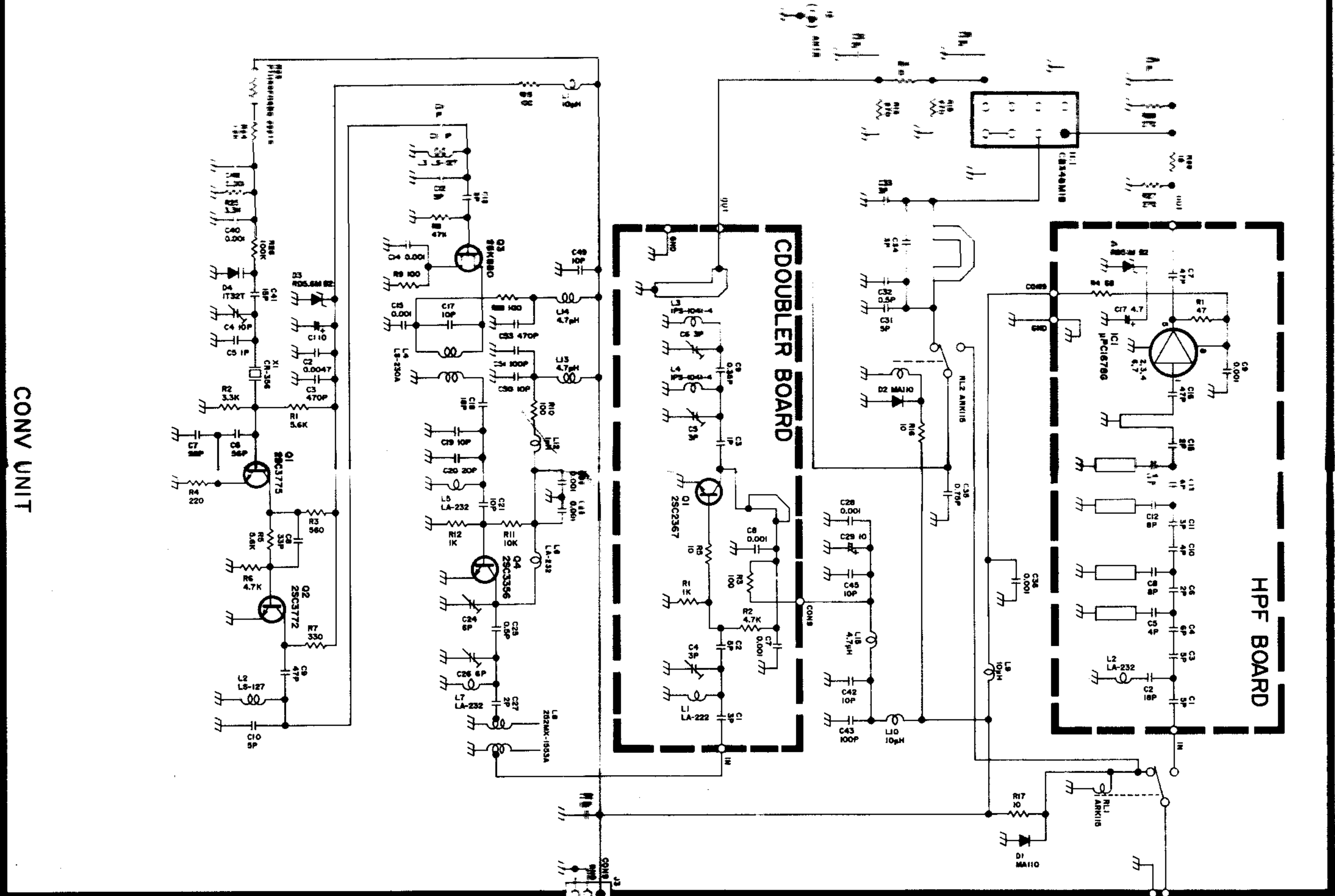


RF UNIT



HPF BOARD





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